

Simulazione background e logica di trigger di JEM-EUSO per implementazione su FPGA

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JEM-EUSO (Extreme Universe Space Observatory onboard Japanese Experiment Module of ISS)

**Observation of high energy
cosmics rays
from space**

Science Objectives

- **Main Objectives :**

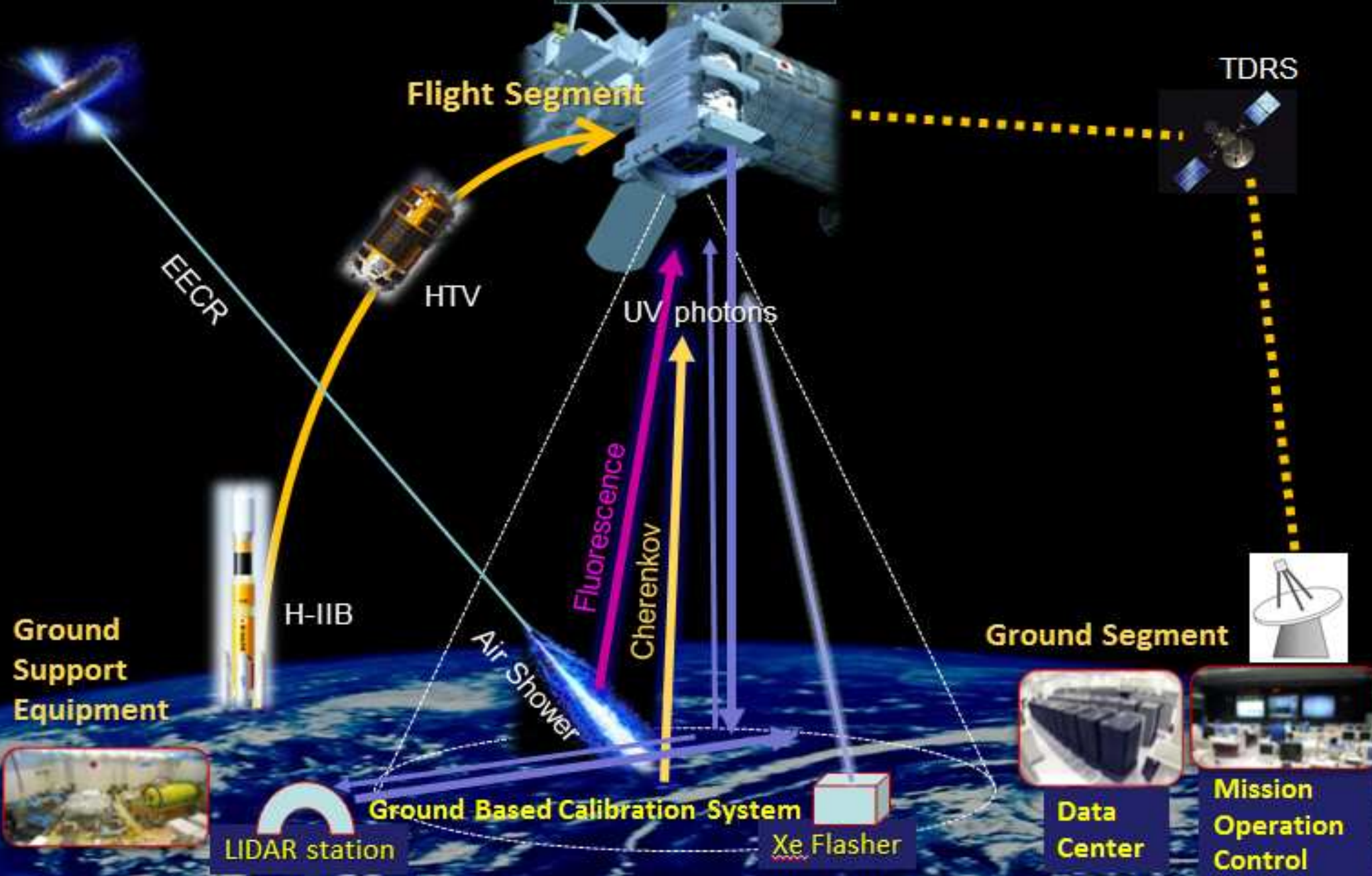
**Astronomy and astrophysics through particle channel
with extreme energies $> 5 \times 10^{19}$ eV**

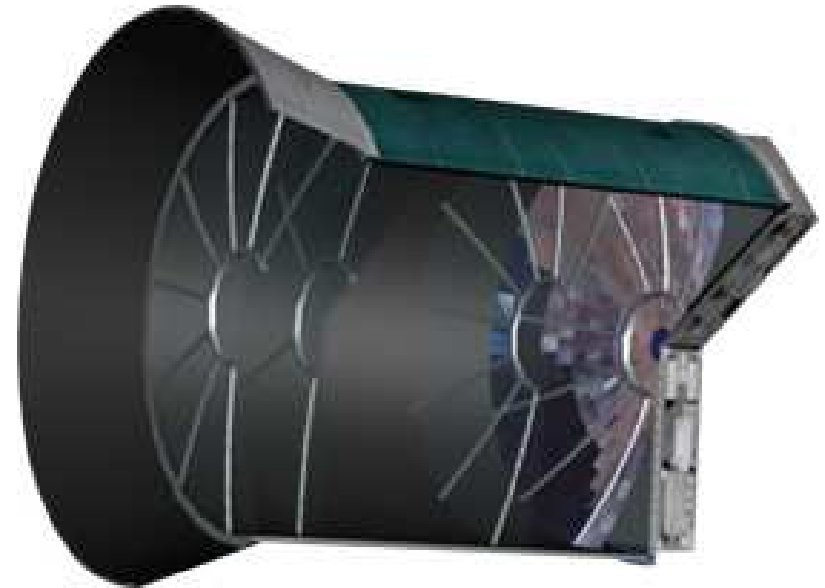
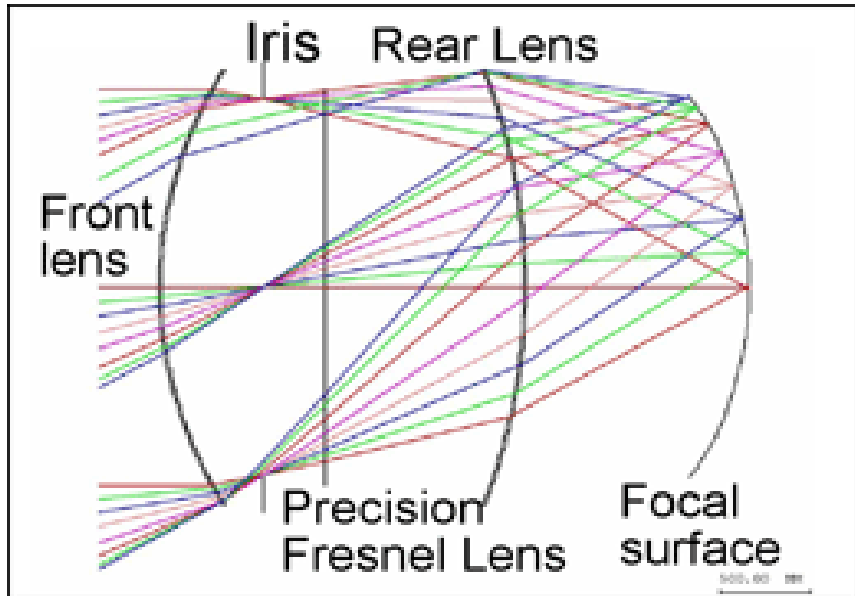
- Identification of individual **sources** by arrival direction
- Measurement of the **energy spectrum** of individual sources
- Understanding of the acceleration processes and source dynamic

- **Exploratory objectives :**

- Detection of extreme energy **neutrinos**
- Measurement of extreme energy **gamma rays**
- Study the intensity and topology of Galactic and extragalactic magnetic fields
- Global observation of **atmospheric** phenomena: nightglows, lightning and plasma discharges. meteors

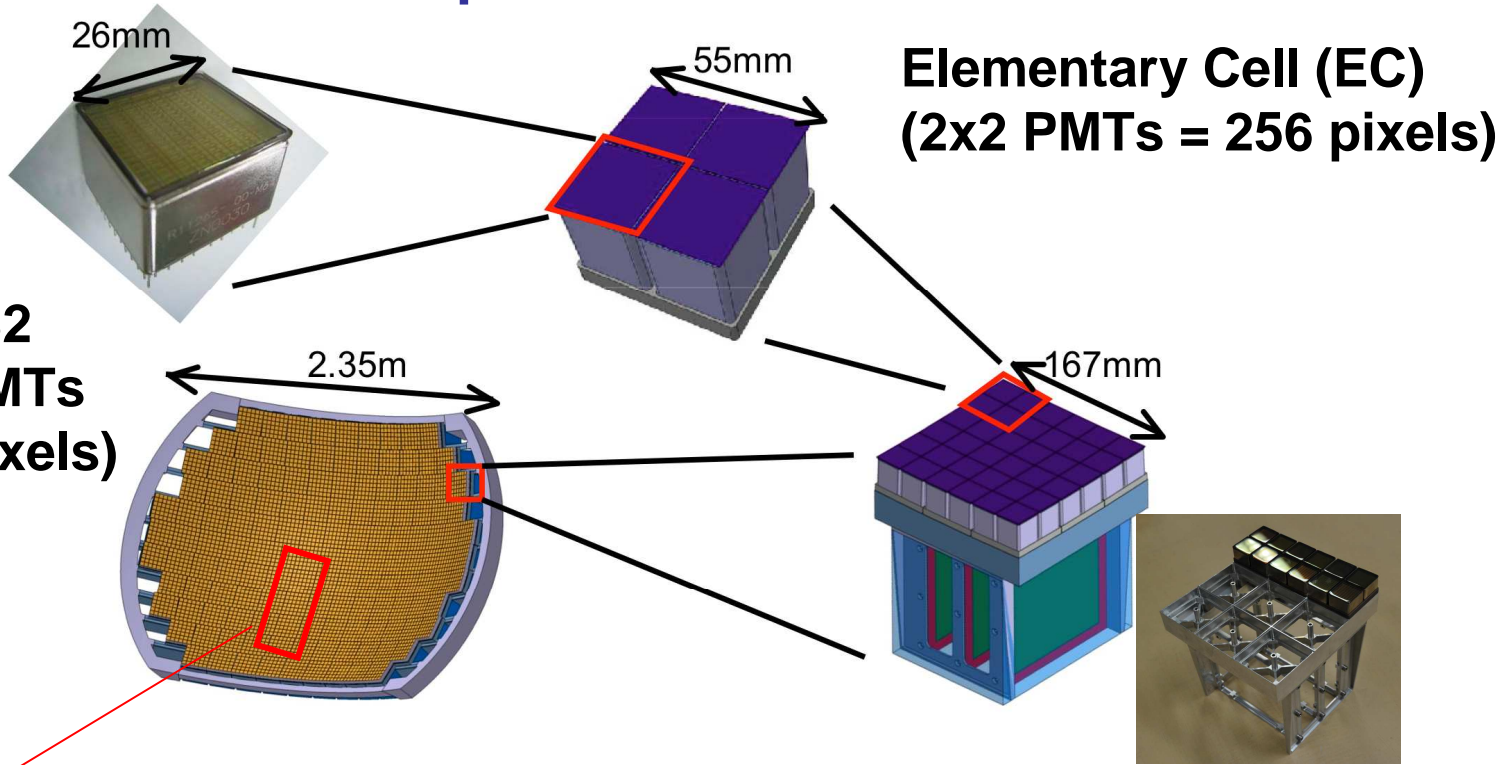
JEM-EUSO





A double Fresnel lens module with 2.5m external diameters is the baseline optics for the JEM-EUSO Telescope, which observes the 300nm - 430nm optical bandwidth. Fresnel lenses (made of radiation hard light-weight plastic material) can provide a large-aperture, wide Field of View (FoV) system with reduced mass and low absorption. Its telescope has a full angle FoV of 60° and a 5 arcmin (=0.075°) angular resolution. This resolution corresponds approximately to (0.50 - 0.60) km on earth, depending on the ISS altitude

Main Components: Focal Surface Detector



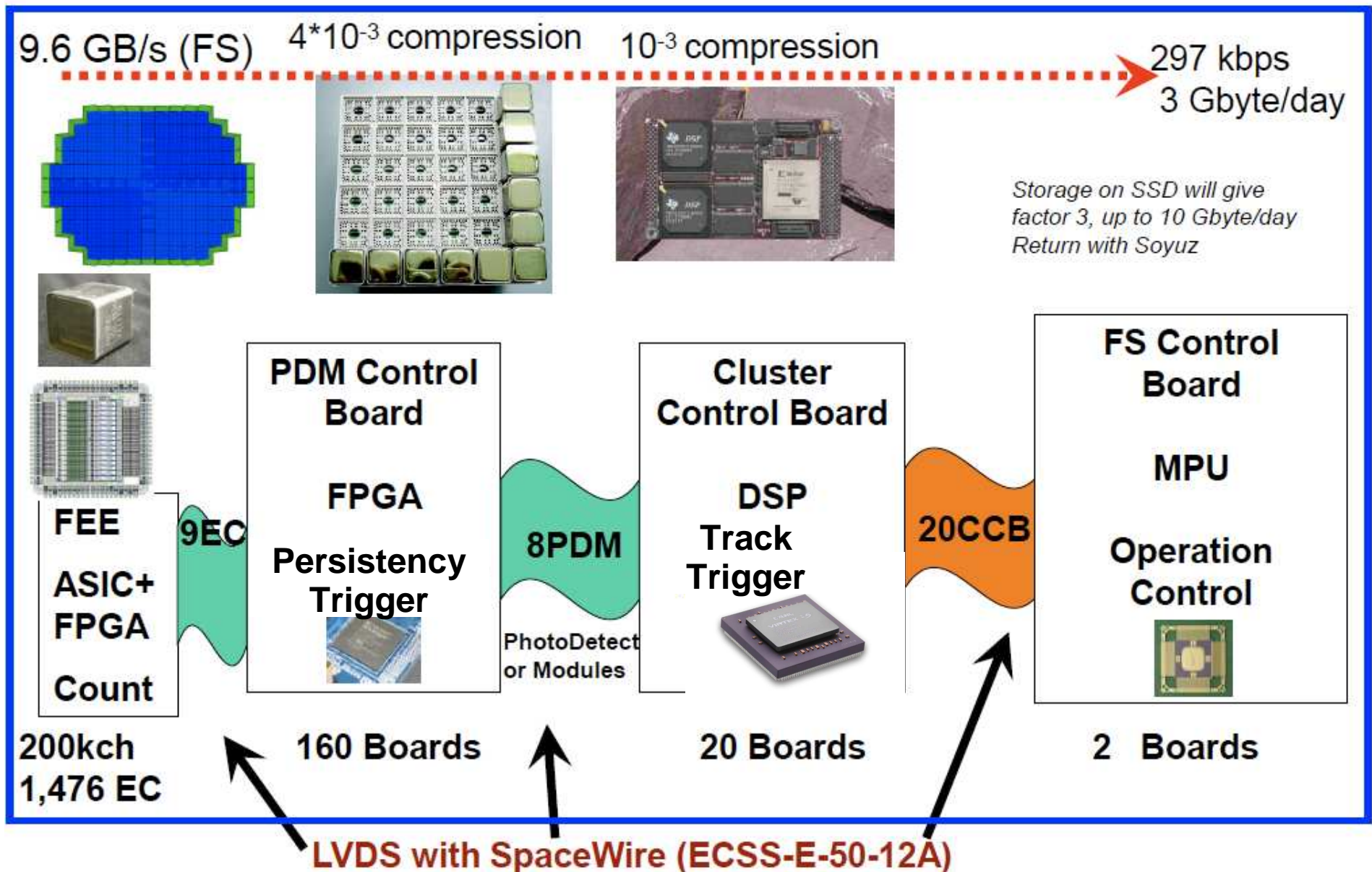
Focal Surface detector
137 PDMs
= 0.3M Pixels

Photo-Detector Module (PDM)
(3x3 ECs = 36 PMTs
2,304 pixels)

1 High Voltage / PDM

Wavelength range : (300-430) nm

JEM-EUSO DAQ – Data reduction block scheme



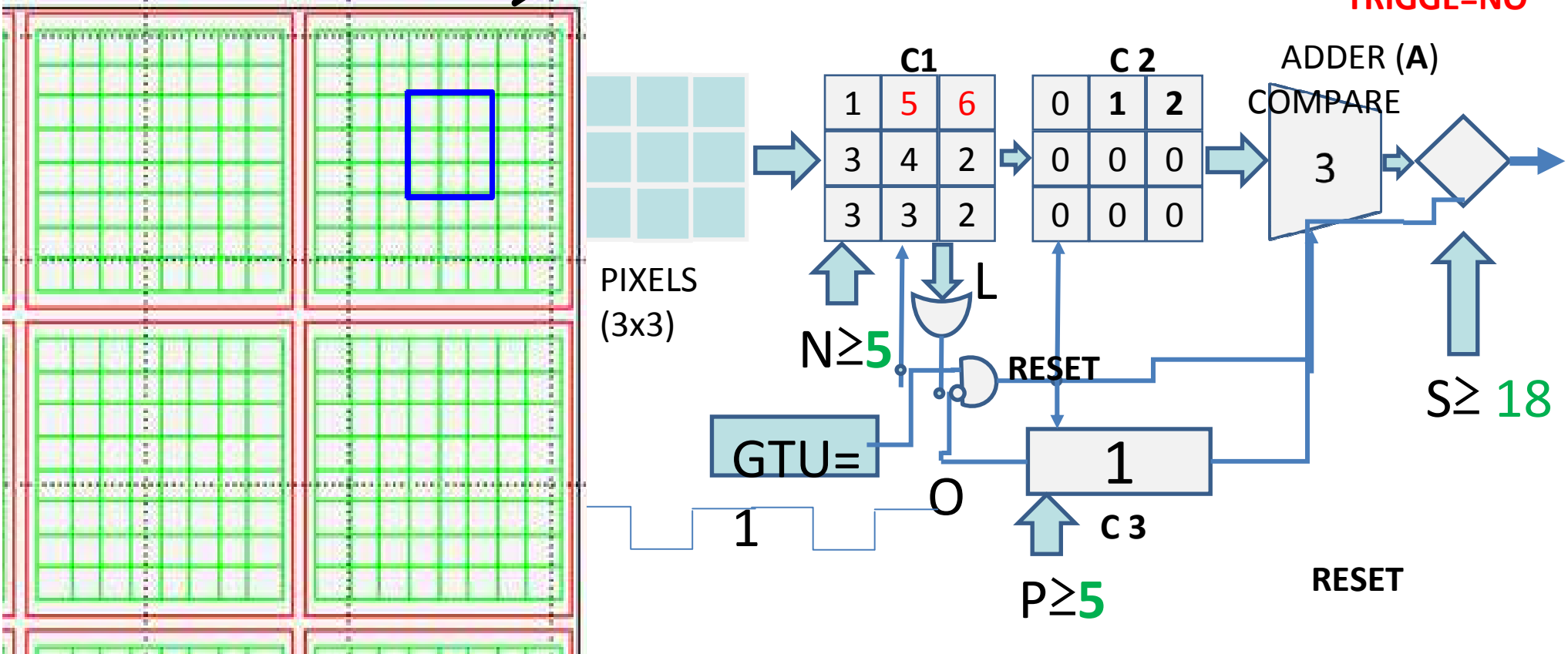
We work on the first-level trigger indicating signal's increase compared with background's standard condition

- Standard Background Value (on the oceans in absence of moon light):

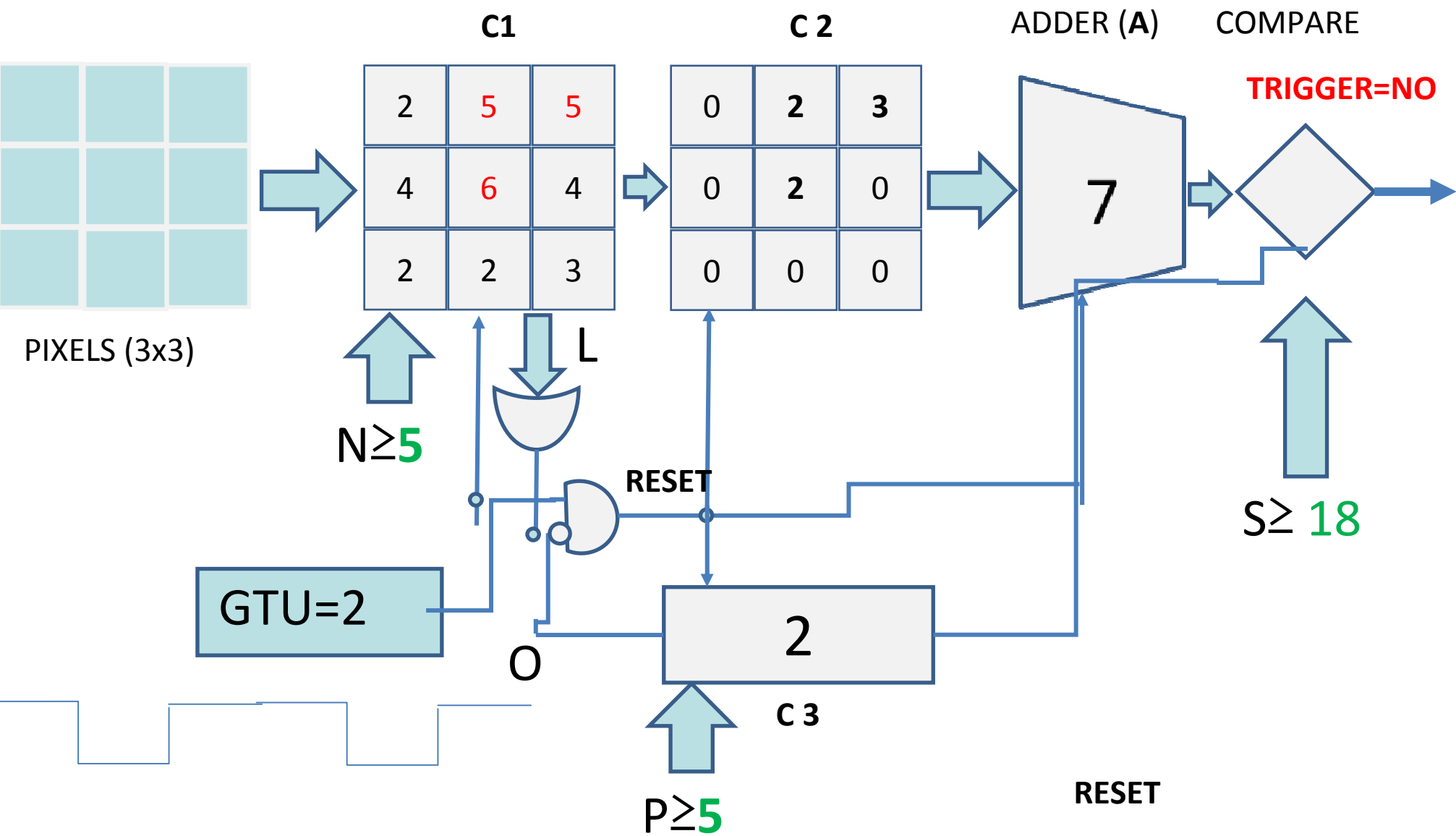
$$\mathbf{500 \text{ ph}/(\text{m}^2 \cdot \text{ns} \cdot \text{sr}) = 1.4 \text{ phe/pix/GTU}}$$

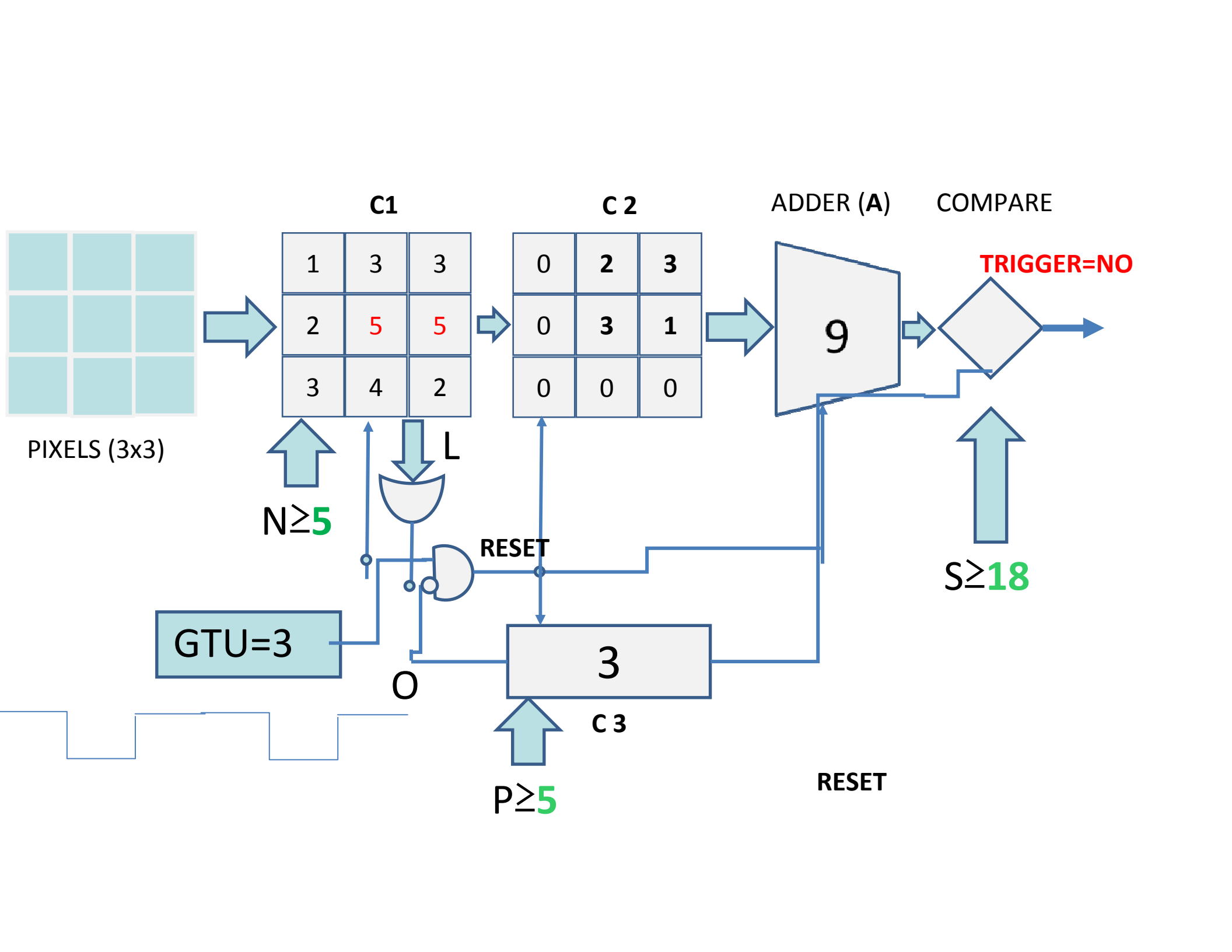
$$(\text{GTU} = 2.5 \mu\text{s})$$

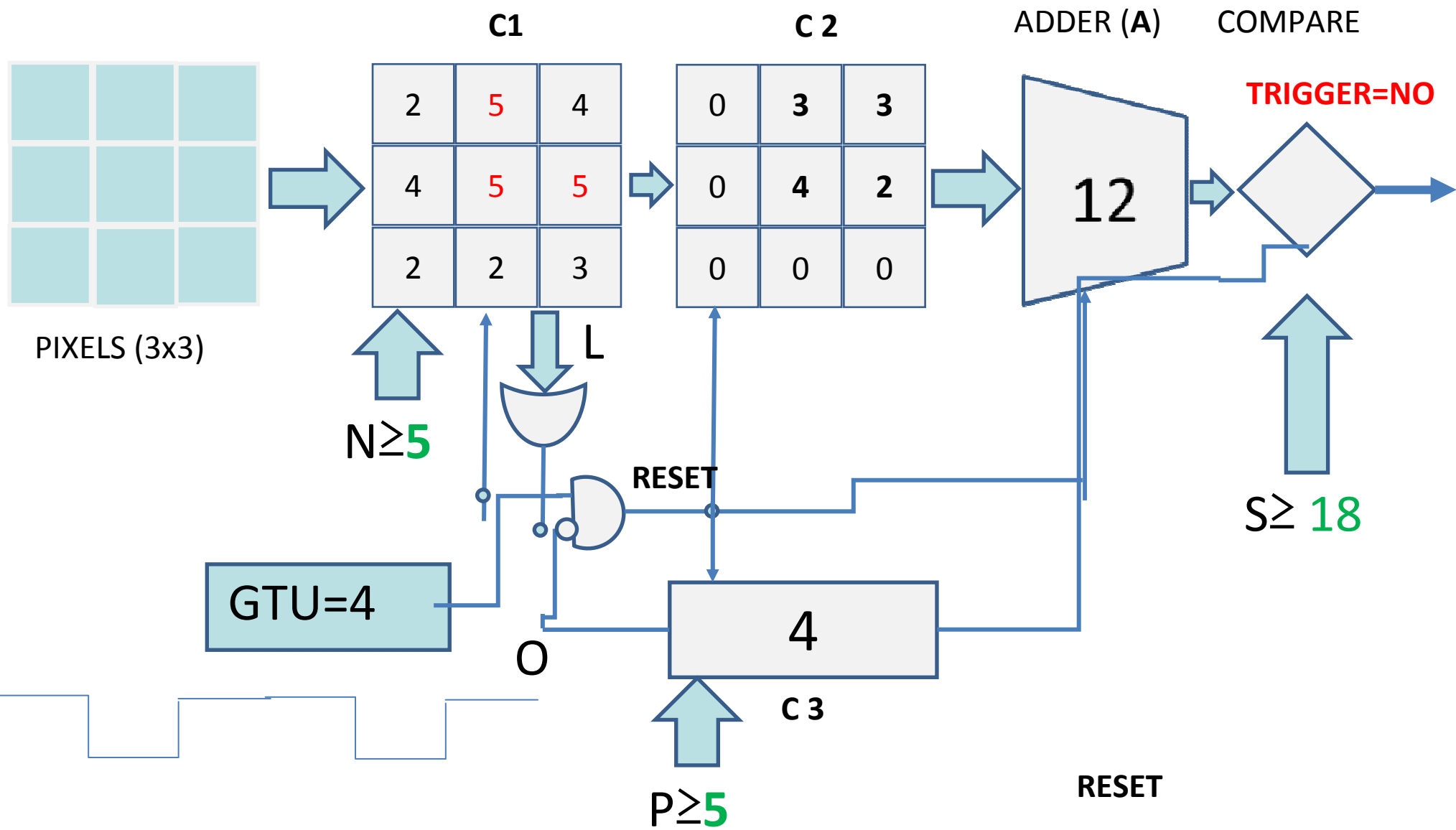
EXAMPLE: TRIGGER

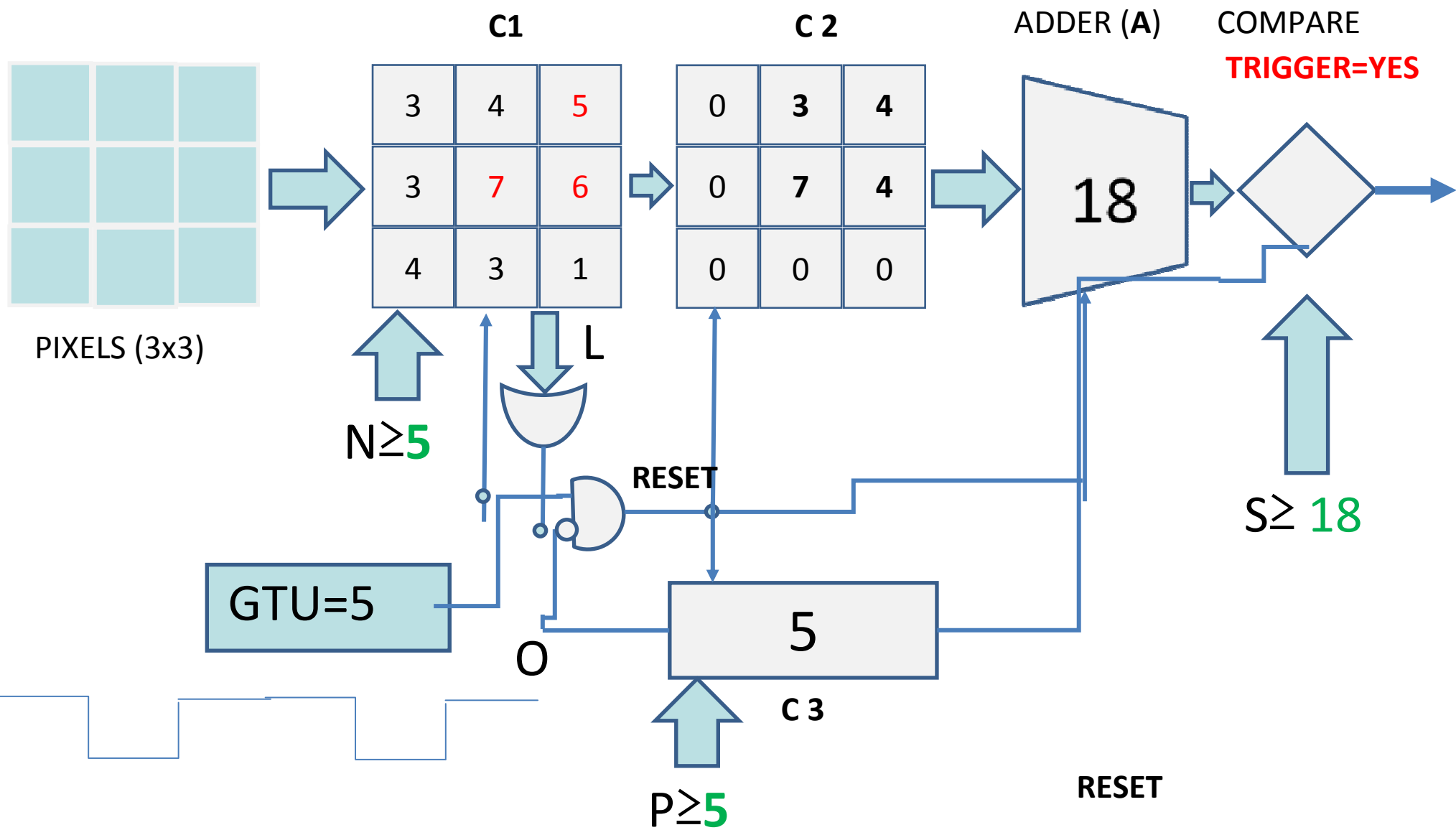


Elementary Cell (256 pixels)







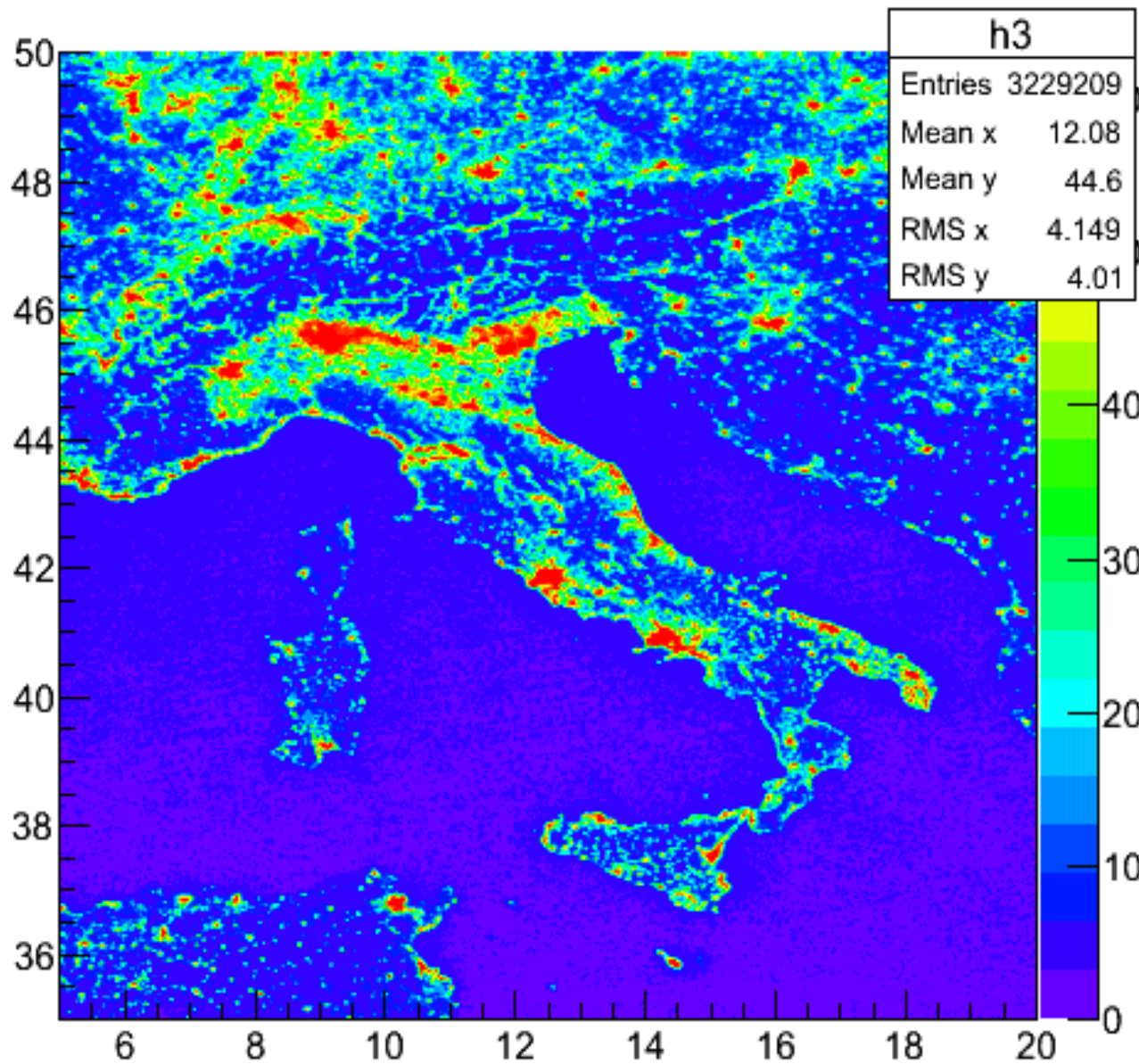




- algorithm tested on FPGA
- rate of trigger for standard conditions background is \sim Hz
- Thresholds values set considering the average of each pixels counts on temporal range \sim ms

- How to set up the threshold value when the UV background changes. This happens for example in correspondence of a city.
- Try to understand how the UV backgrounds could change

Background's simulation of cities with map created using data from DMSP(Defense Meteorological Satellites Program)



- Visible pixels are relative value ranging from 0 to 63

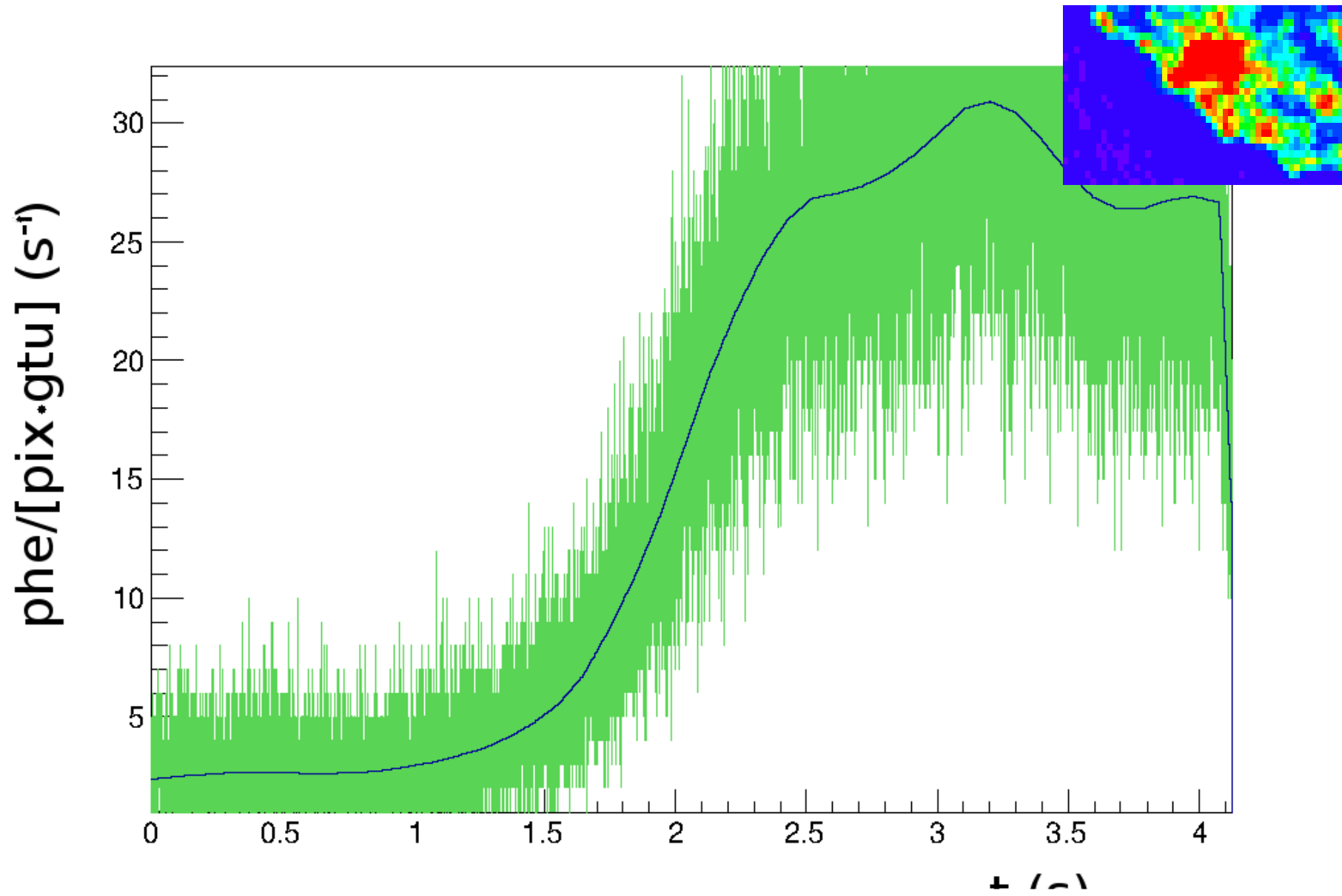
- the visible telescope is sensitive to radiation from (0.35-2) μm

- telescope resolution : 0.60 km

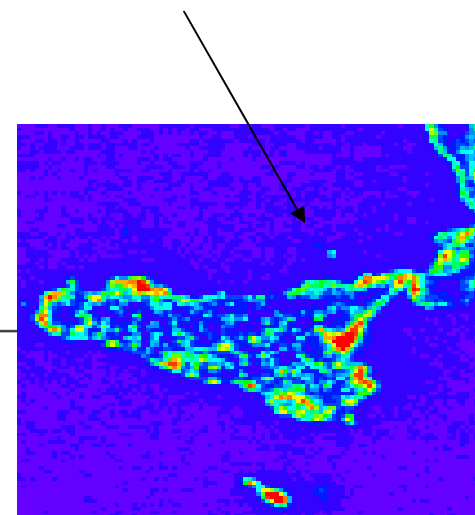
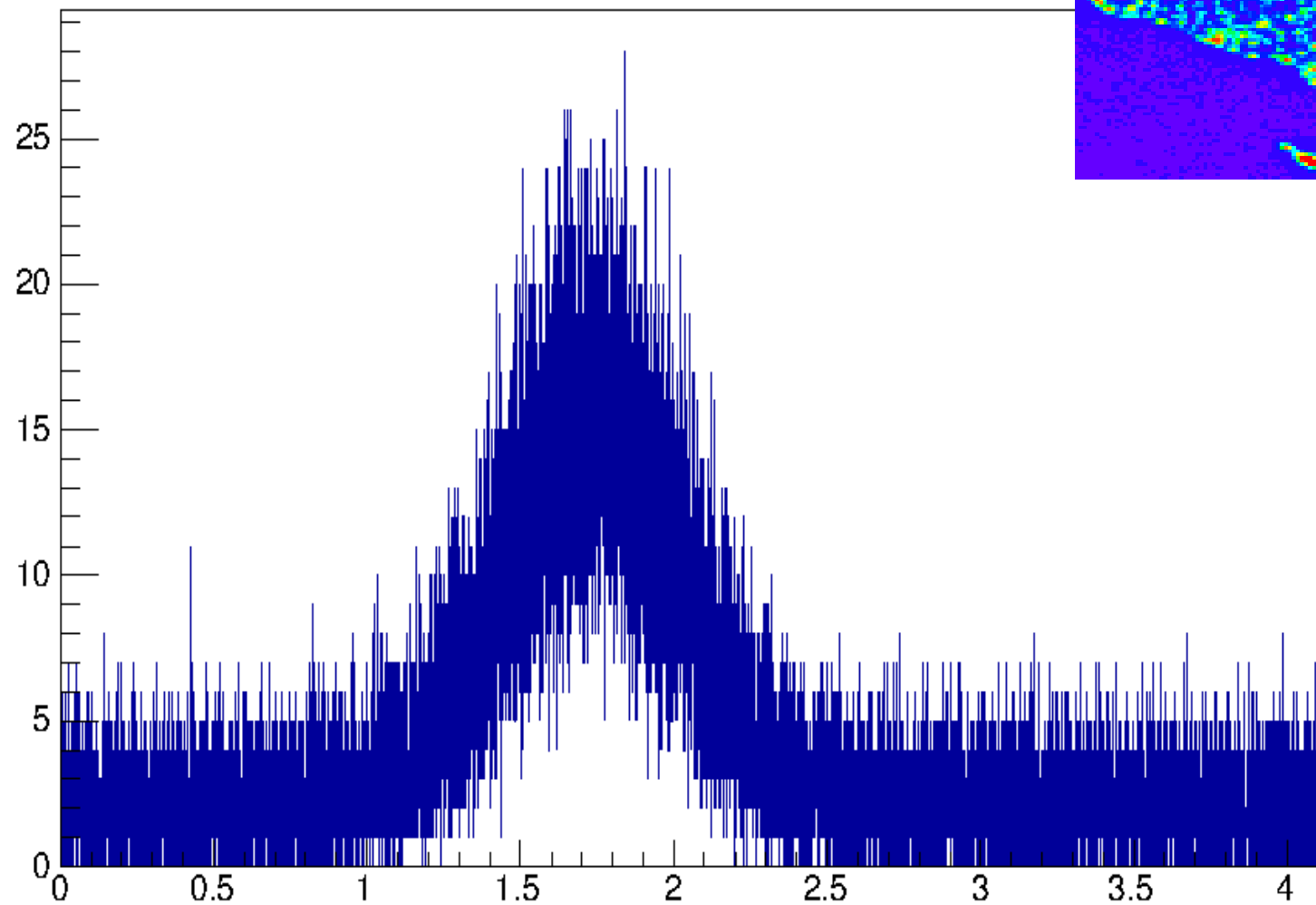
$$V_{DMSP} \times \frac{1}{2.7} = \frac{1}{1.4(\text{phe/pix/gtu})}$$

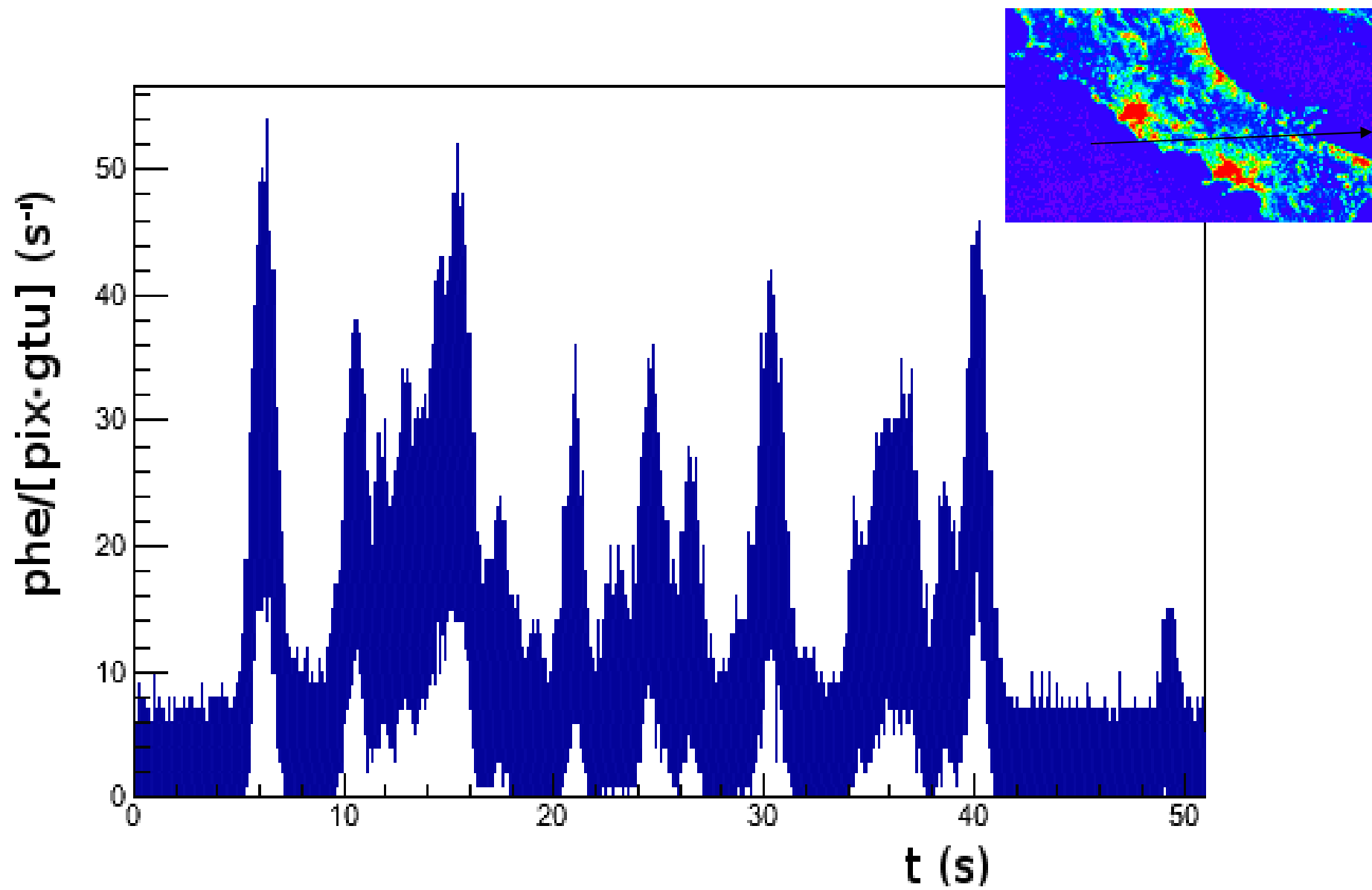
Background simulation of Rome Area

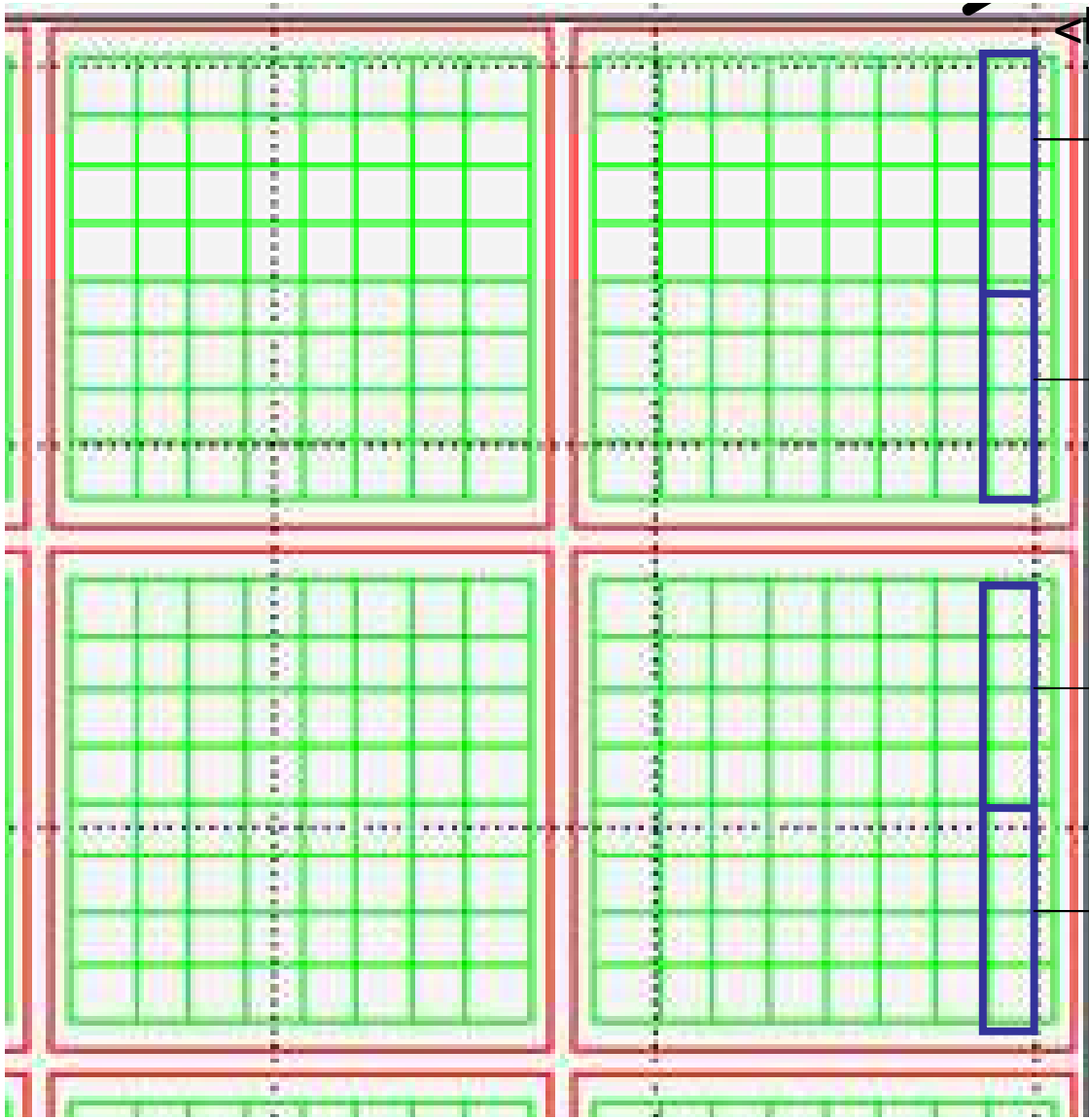
Example of 1 pixel



phe/[pix·gtu] (s⁻¹)







<bckg> calculated on 1024GTU (2.56ms)

THR1

Take the highest value among
THR1-4 = THR_{1024}

THR2

If $THR_{1024} > THR$

=> $THR = THR_{1024}$

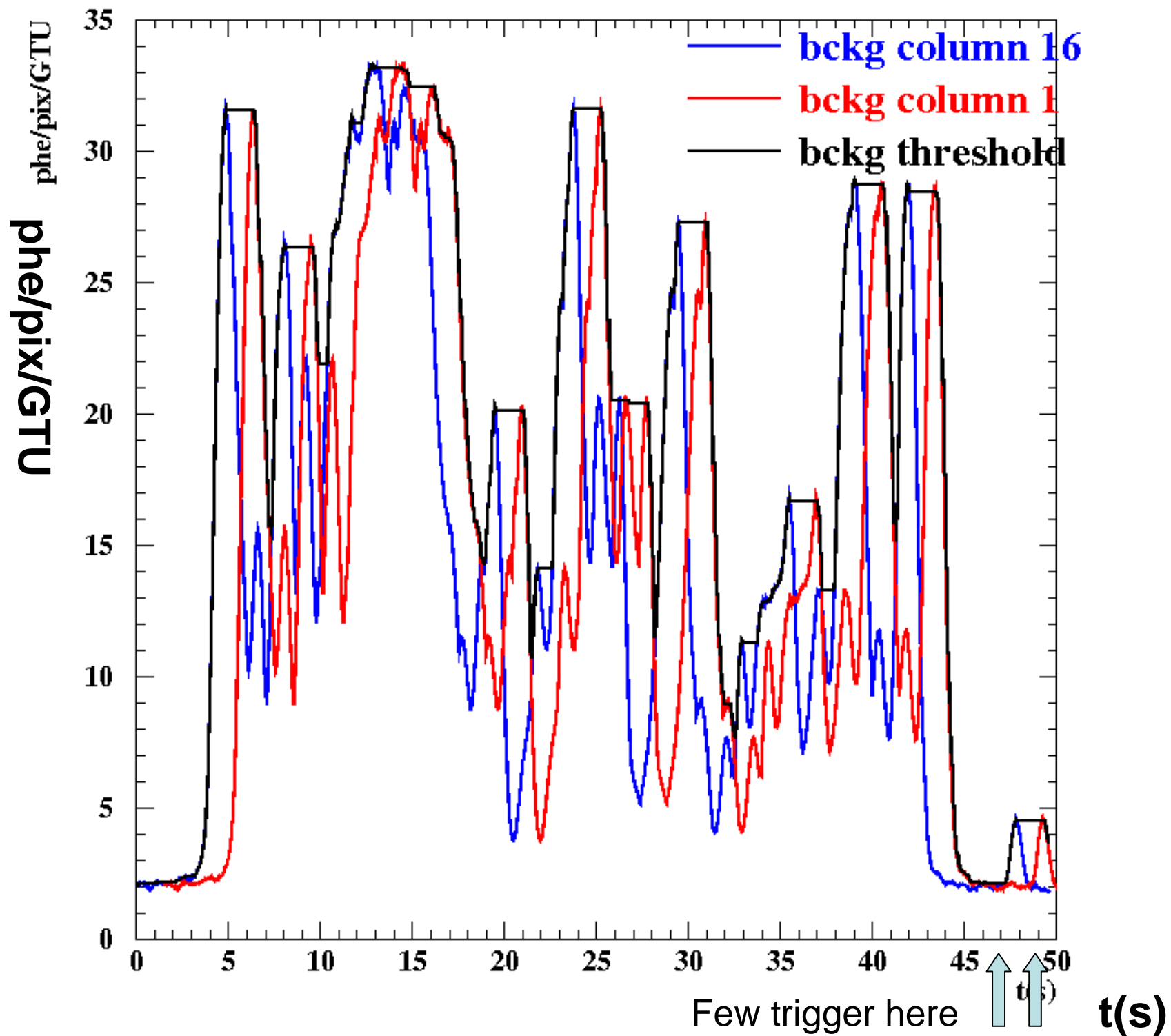
THR3

Every 31 cycles
(=79.36ms) store
THR1024 in a 16
values deep cycle
memory.

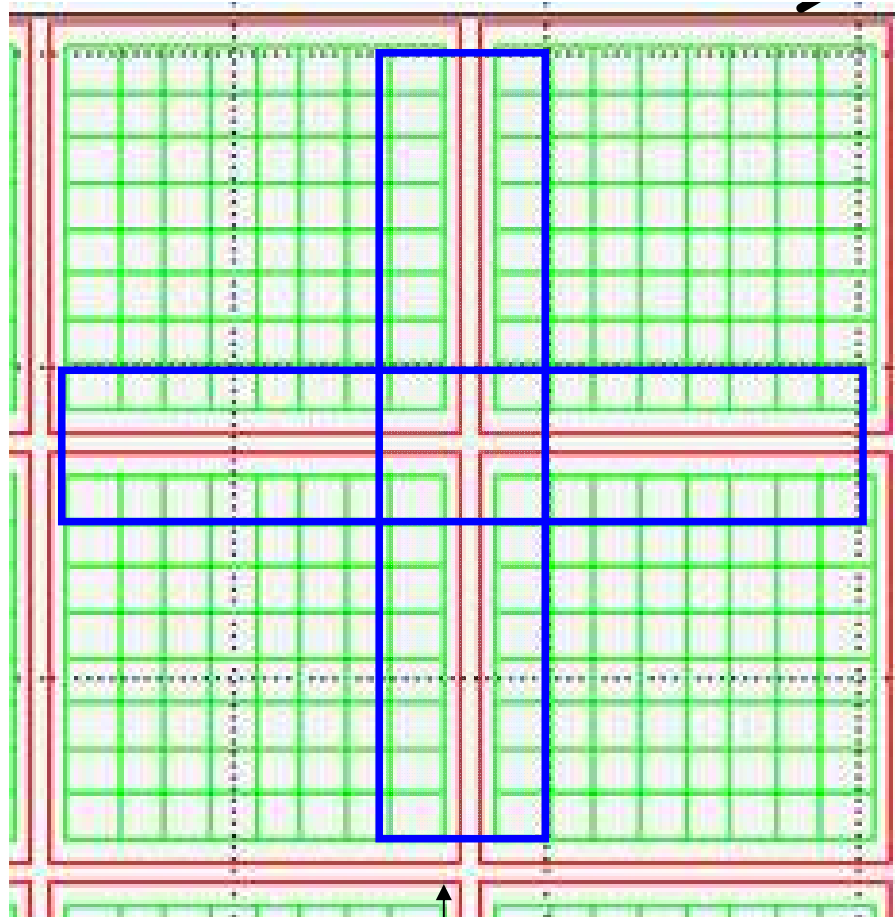
THR4

This is the time that
a pixel takes to
change completely
its FoV. In this
way we have 16
values that give
the <bckg> in
each column of
pixels





Optimization of power consumptions for FPGA



1 EC

FPGA use : 9 %

Static Power: 3131 mW

Dynamic Power : 149 mW

1 PDM (9 EC)

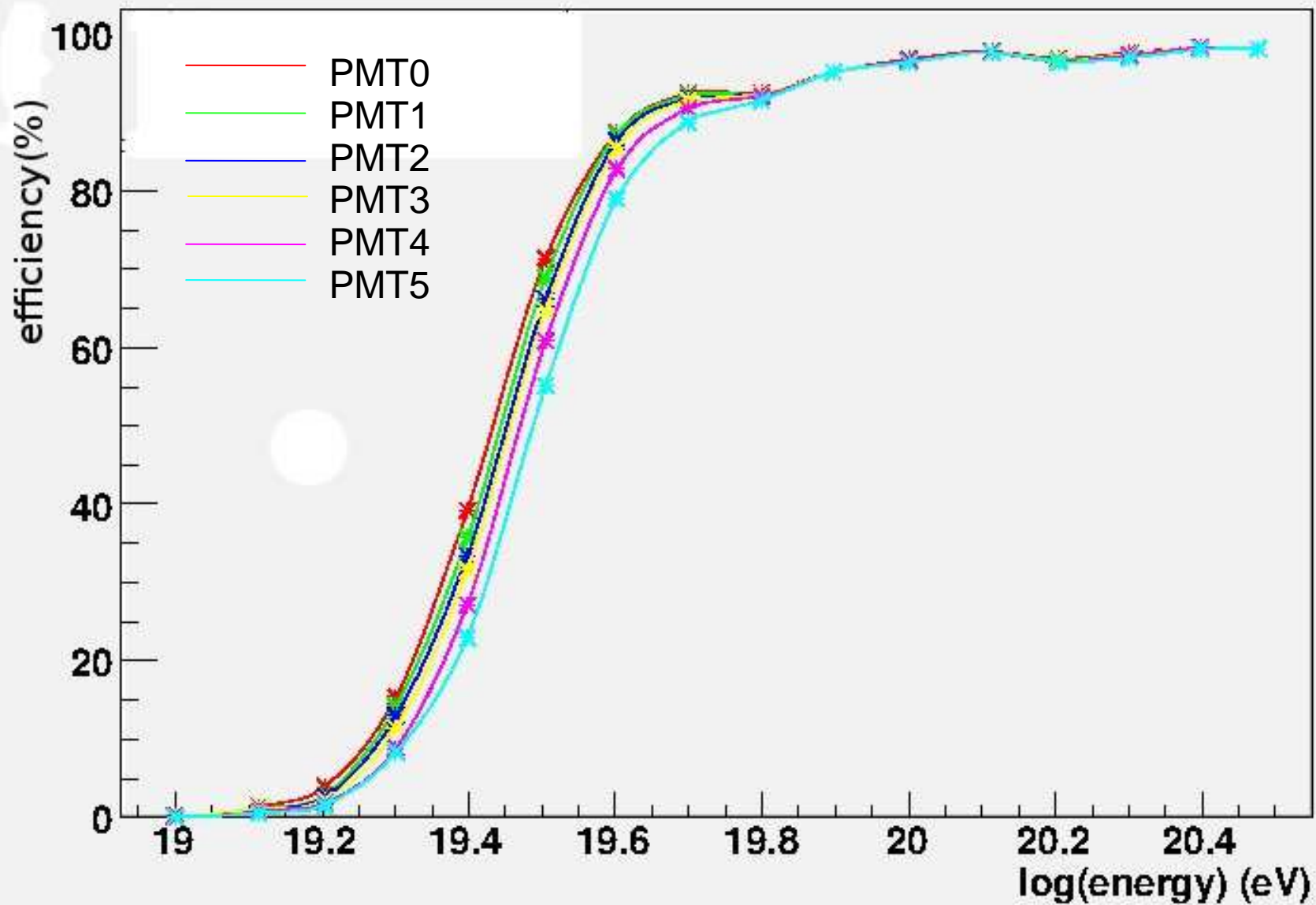
FPGA use : 80 %

Total Power: 4768 mW

Dynamic Power : 1588 mW

PMT1 Configuration →

FPGA use: 60%



Conclusions

- it was simulated a change of background due the presence of cities
- it was realized that the trigger algorithm was inefficient for a sudden background increase
- trigger algorithm has been modified in order to have a dynamic setting of the threshold value adapting to the Background conditions
- to limit the consumption of FPGA resources new configuration were tested, PMT1 in particular does not preclude the trigger efficiency

Ringraziamenti

Prof. Mario Edoardo Bertaina

Dott. Luca Latronico