



First FBK production of 50 μm ultra-fast silicon detectors

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ABSTRACT

Fondazione Bruno Kessler (FBK, Trento, Italy) has recently delivered its first 50 μm thick production of Ultra-Fast Silicon Detectors (UFSD), based on the Low-Gain Avalanche Diode design. These sensors use high resistivity Si-on-Si substrates, and have a variety of gain layer doping profiles and designs based on Boron, Gallium, Carbonated Boron and Carbonated Gallium to obtain a controlled multiplication mechanism. Such variety of gain layers will allow identifying the most radiation hard technology to be employed in the production of UFSD, to extend their radiation resistance beyond the current limit of $\phi \sim 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$. In this paper, we present the characterisation, the timing performance, and the results on radiation damage tolerance of this new FBK production.

1. Introduction

Ultra-Fast Silicon Detectors (UFSD) [1–3] are innovative silicon sensors optimised for timing measurements based on the Low-Gain Avalanche Diode (LGAD) [4] design. LGAD merges the best characteristics of traditional silicon sensors with the main feature of Avalanche Photodiodes (APD), using n-in-p silicon diodes with a low and controlled internal multiplication mechanism. To obtain charge multiplication, an electric field of the order of $E \sim 300 \text{ kV/cm}$ is required, under this condition the electrons (and to less extent the holes) acquire enough kinetic energy that are able to generate additional e/h pairs. The required field value is obtained by implanting an appropriate acceptor density ($N_A \sim 10^{16}/\text{cm}^3$) under the n⁺⁺ cathode. The thickness of the acceptor implant is of the order of 1 μm .

UFSD recently obtained a time resolution of $\sigma_t \sim 30 \text{ ps}$ in beam tests [5] and are now being considered in the upgrade of the CMS and ATLAS experiments as timing detectors [6,7].

After the successful production of the first batch of 300 μm thick UFSD in 2016, Fondazione Bruno Kessler (FBK) delivered its first 50 μm thick UFSD. The 50 μm production includes a variety of doping profiles and strategies of gain layers to identify the most radiation hard technology for UFSD, to enhance their radiation tolerance beyond the current limit of $\phi \sim 10^{15} \text{ n}_{\text{eq}}/\text{cm}^2$ [8].

2. Production of 50 μm thick UFSD at FBK

The first production of UFSD at FBK was completed in 2016 on a 300 μm substrate (referred to as FBK UFSD1), and included several type

of structures, having both AC and DC-coupled devices [9,10]. The goals of the production were the good control of the low gain mechanism and the good correspondence between measurements and simulation of the gain as a function of the gain layer doping concentration. The agreement between data and simulation proved the success of this first production.

In 2017 FBK applied the know-how gained through the 300 μm production to thinner substrates, and manufactured UFSD on 60 μm thick Si-on-Si substrates 6 inch wafers (namely FBK UFSD2). After thermally bonding the high resistivity (>3000 $\Omega \text{ cm}$) p-type Float-Zone wafers to the 500 μm thick supports, the device active thickness reduces by $\sim 5 \mu\text{m}$. The main goals of the 2017 production were to establish a reliable design for UFSD on thin substrates and to test solutions to enhance the radiation tolerance of UFSD devices.

Recent radiation damage studies on UFSD from different manufacturers [11] show that the doping of the gain layer becomes progressively deactivated by irradiation. In particular, it has been shown that the boron atoms are still present in the gain layer volume, but they switch from substitutional to interstitial, not contributing to the gain mechanism anymore. Within the RD50 Collaboration [12], it has been proposed to replace the Boron of the gain layer with Gallium, as Gallium might have a lower probability to become interstitial than Boron [13,14]. A further proposal has been to add Carbon atoms in the gain layer volume to reduce the disappearance of gain, as Carbon might slow down the acceptor removal mechanism protecting the Boron, or Gallium, dopant [15]. Moreover, the idea that reducing the implant volume could reduce the cross section of gain layer inactivation was proposed. All these solutions have been tested in the 50 μm FBK

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Table 1
Summary of gain layer implant strategy.

Wafer #	Dopant	Dose	Carbon	Diffusion
1	Boron	0.98		Low
2	Boron	1.00		Low
3	Boron	1.00		High
4	Boron	1.00	Low	High
5	Boron	1.00	High	High
6	Boron	1.02	Low	High
7	Boron	1.02	High	High
8	Boron	1.02		High
9	Boron	1.02		High
10	Boron	1.04		High
11	Gallium	1.00		Low
12	Gallium	1.00		Low
13	Gallium	1.04		Low
14	Gallium	1.04		Low
15	Gallium	1.04	Low	Low
16	Gallium	1.04	High	Low
18	Gallium	1.08		Low
19	Gallium	1.08		Low

production, following a complete simulation of Gallium and Carbon implantation and diffusion.

Therefore, in the latest 50 μm thick UFSD production at FBK we have simulated and manufactured devices with 5 different gain layer configurations, summarised in Table 1: (i) Boron Low Diffusion, (ii) Boron, (iii) Boron with Carbon, (iv) Gallium, and (v) Gallium with Carbon. The Boron implants have 4 doping doses, where 1.00 is the reference implant dose as used in FBK UFSD1 production [10], and other splits are obtained in steps of 2% from the reference dose. Two different diffusion temperatures (Low and High) have been used, to vary the implant volume of the gain layer. The Gallium have 3 implant doses of the gain layer (step of 4%, starting from the reference dose). Only one diffusion temperature was used, same as Boron Low, given the higher diffusivity of Gallium. The Carbon implants have 2 doses of concentration (Low and High), and was implanted inside the gain layer volume prior than Boron or Gallium. No thermal annealing was performed to Carbon before the acceptor implantation.

The bulk dopant concentration is $\sim 3 \cdot 10^{12}/\text{cm}^3$.

3. Characterisation of FBK UFSD2 production

Extensive quality tests and electrical characterisation on wafers have been made at FBK. The studies concentrated on single pad sensors with an active area of $1 \times 1 \text{ mm}^2$.

3.1. Current versus reverse bias

Fig. 1 shows the dark current as a function of the reverse bias measured at room temperature, on sensors from all the produced wafers. Below 20 V the surface current is measured; between 20 and 30 V the current shows a rapid increase (ankle) due to the depletion of the gain layer and ~ 5 V later (knee) flattens due to the depletion of the bulk; above 30 V sensors are fully depleted and the exponential growth of the dark current is due to the relationship between gain and the reverse bias [16], as the charge multiplication follows:

$$N(x) = N_0 \cdot e^{\alpha x} = N_0 \cdot G, \quad (1)$$

where x is the length travelled by the charge and G is the gain as function of α , the impact ionisation rate, which depends exponentially on the electric field E . The two families of curves separated by a factor ~ 100 are due to different production batches of the Si-on-Si wafers, namely *good* with low dark current and *leak* with high dark current as listen on Table 2, and not to differences in the production process.

More in detail, Fig. 2 shows the effects of Carbon on wafers doped with Gallium. An implant of Low-Carbon dose does not affect the electrical characteristics of the sensors, but slightly increases the dark current

Table 2
List of wafers belonging to good or high leakage production batches.

Batch	Wafer #
Good	W1, W2, W4, W6, W7, W8, W9, W11, W12, W13, W17, W19
Leak	W3, W5, W10, W14, W15, W16, W18

and shifts the depletion of the gain layer towards lower voltages. On the other hand, the High-Carbon dose sensor is dominated by the dark current, with an almost constant current. Wafers with Boron gain implant show very similar behaviour.

Dark current measurements at different temperatures have been performed. Decreasing the operating temperature of the sensors reduces the overall dark current while increasing the internal gain of UFSD (for more details, see [17]), as G from Eq. (1) is influenced by the temperature by means of the carriers saturated velocities and the exponential temperature dependence of the impact ionisation rate, α . This effect is responsible for a earlier sensor breakdown, as clearly visible in Fig. 3 for Wafer 8.

3.2. Capacitance versus reverse bias

Fig. 4 shows the capacitance as a function of the reverse bias measured at room temperature, for sensors with different gain layer configurations. Similarly to what already observed in Fig. 1, C-V curves show a drop at a voltage corresponding to the depletion of the gain layer, while the point at which the curves flatten indicates the full depletion of the sensor, when also the high-resistive bulk is depleted.

As for standard silicon sensors, UFSD with a reverse bias can be considered as a parallel plate capacitor, where the distance between the capacitor plates is the width of the depletion region, and the area of the plates is the active area of the sensors. The capacitance C , of the sensor is related to the reverse bias V , and the acceptor active doping concentration N_A , as given by the relation:

$$C \propto \sqrt{N_A/V}. \quad (2)$$

Therefore, assuming the same purity of the bulk for all wafers, the point at which the capacitance drops is directly related to the active doping concentration of the gain layer.

From Fig. 4, it is possible to distinguish the family of Gallium doped sensors (dashed lines), with different doping concentration, and the family of Boron doped sensors (plain lines), as listed in Table 1. Moreover, the effect of Carbon implantation is visible: for Low Carbonated sensors (open squares) a small reduction of the active doping concentration is noticeable, while for High Carbonated sensors the active doping concentration of the gain layer is dramatically reduced.

3.3. Acceptor doping concentration versus depth

From the measurements of capacitance presented in Section 3.2 and exploiting the relation

$$N(w) = \frac{2}{q\epsilon_{Si}A} \frac{1}{d(1/C^2)/dV} \quad \text{where} \quad w = \frac{\epsilon_{Si}A^2}{C(V)} \quad (3)$$

it is possible to extract the active doping density, N , as a function of the depletion depth, w , being A the sensor area and ϵ_{Si} the Silicon permittivity.

Figs. 5 and 6 show the doping densities as a function of the sensor depth for Boron and Gallium based gain implants, respectively (plots are on scale). The origin of the x -axes corresponds to the p-n⁺⁺ junction. As already observed from I - V and C - V measurements, the addition of Carbon, with low concentration, in the gain layer volume slightly reduces the active doping concentration, and the reduction is more evident in sensors with Gallium. The addition of a high concentration of Carbon results in a strong reduction of the gain layer, bringing the gain of those sensors to be ~ 1 . Also, it is visible the different diffusion

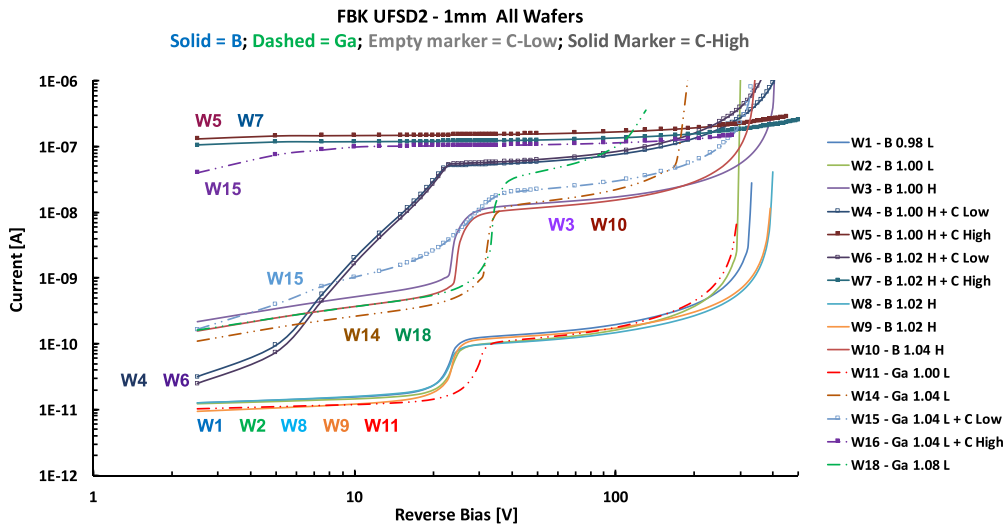


Fig. 1. Dark current as a function of the applied reverse bias for all the wafers produced in UFSD2. Solid lines indicate wafers with Boron, dashed lines wafers with Gallium; open squares indicate Low Carbon, solid squares High Carbon adjunction in the gain layer volume.

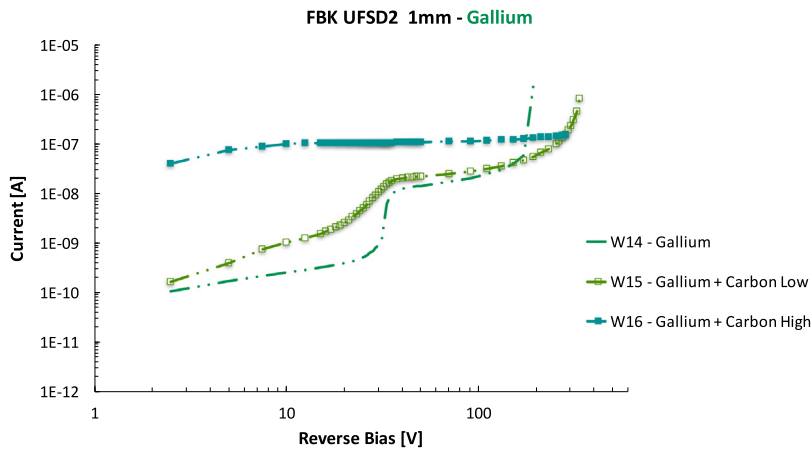


Fig. 2. Dark current as a function of the applied reverse bias for 3 different configurations of Gallium doped wafers: without Carbon (no markers), Low Carbon (open squares), High Carbon (solid squares). Both No-Carbon and Low-Carbon sensors come from the high leakage batch of wafers.

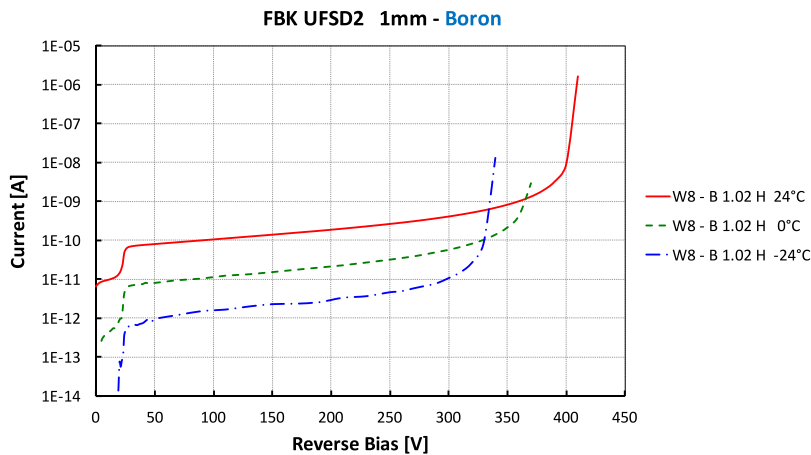


Fig. 3. Dark current as a function of the applied reverse bias of Wafer 8 measured at 3 different temperatures: +24 °C (plain line), 0 °C (dashed line), -24 °C (dashed–dotted line).

of Gallium with respect to Boron, with a broader and deeper peak, resulting in a lower peak concentration. It will be shown in Section 4.1, that despite the lower peak concentration, the electric field and thus the gain of sensors with Gallium is higher than the one with Boron,

as the Gallium implant is deeper and the high electric field region is broader. Referring to the Boron only case, it is interesting to notice how the different diffusion temperatures impact on the width and the height of the doping peak. Keeping the implantation dose constant (see

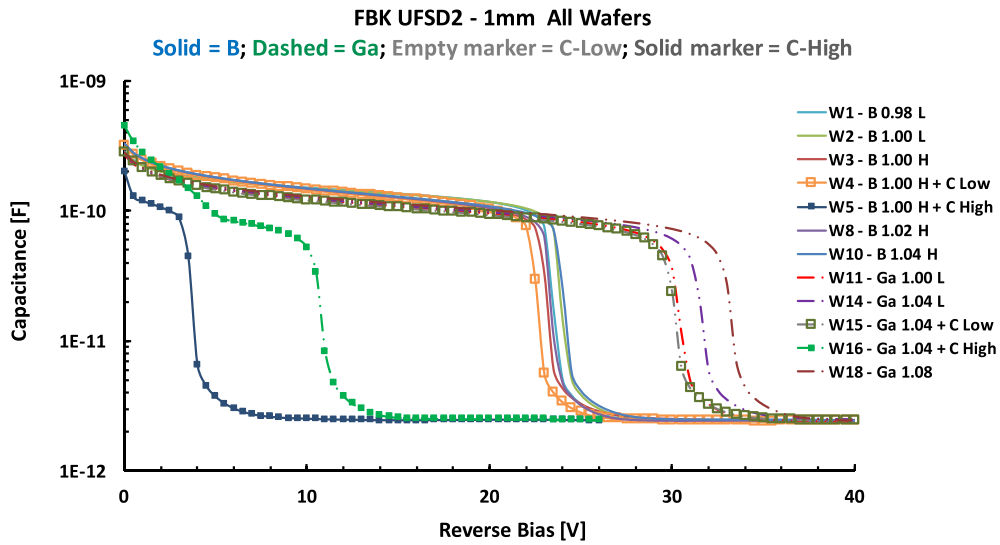


Fig. 4. Capacitance as a function of the applied reverse bias for sensors with different gain layer configurations. Solid lines indicate wafers with Boron, dashed lines wafers with Gallium; open squares indicate Low Carbon, solid squares High Carbon adjunction in the gain layer volume.

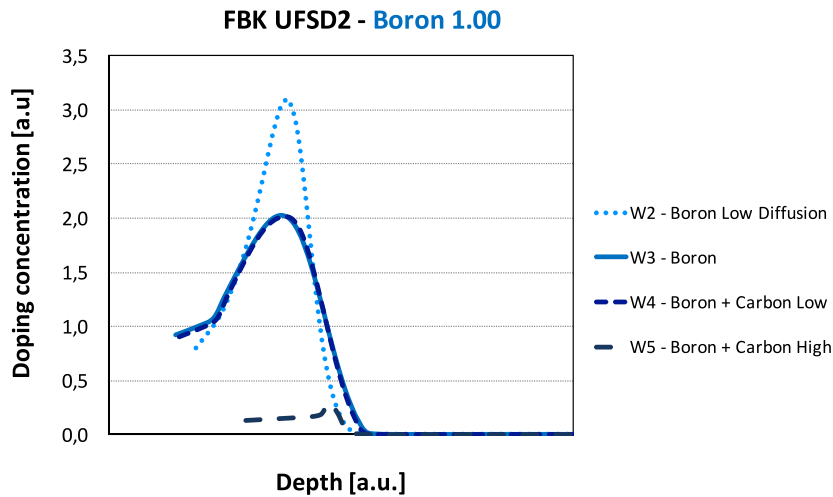


Fig. 5. Active dopant concentration as a function of the UFSD depth for Boron doped gain implants, comparing Boron only, Low Diffusion (dotted line) and standard Diffusion (plain line), Boron plus Low Carbon (dashed line) and Boron plus High Carbon (dotted–dashed line).

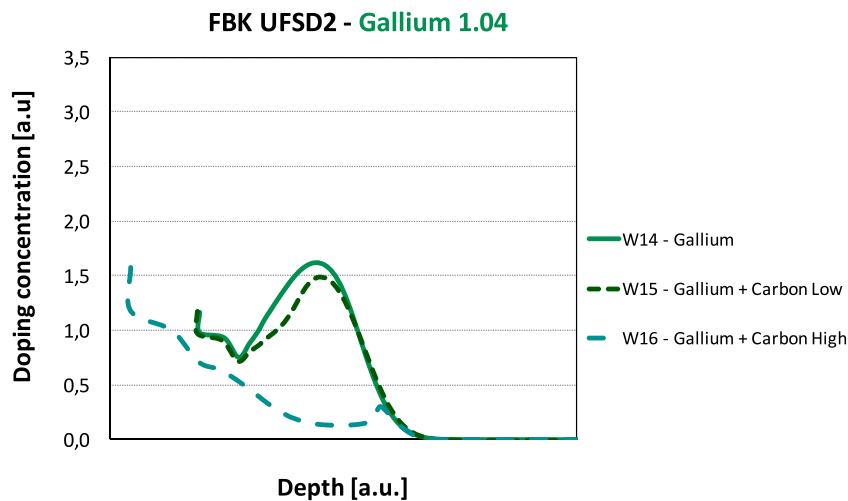


Fig. 6. Active dopant concentration as a function of the UFSD depth for Gallium doped gain implants, comparing Gallium only (plain line), Gallium plus Low Carbon (dashed line) and Gallium plus High Carbon (dotted–dashed line).

Table 1), Boron Low Diffusion is the configuration resulting in highest gain, for a given reverse bias.

4. Laboratory measurements on FBK UFSD2 production

Laboratory measurements are performed through a Transient Current Technique (TCT) setup, inducing a signal into the sensors using an infrared picosecond laser, with a wavelength of 1060 nm. The laser can be focused with a lens and the minimum diameter of the laser spot is 10 μm . The sensor output signal is amplified by a factor 100 using a Cividex Broadband Amplifier, a low-noise current amplifier with an analog bandwidth of 2 GHz, and readout by a Lecroy Oscilloscope (740Zi) with a sampling of 40 GSample/s and a bandwidth of 4 GHz.

4.1. Gain measurement

In the 50 μm UFSD FBK production, standard silicon diodes with no gain implant (PiN) are paired to each $1 \times 1 \text{ mm}^2$ UFSD single pad. The PiN diode is used to measure the gain of the UFSD, by shooting both the PiN and the UFSD with the infrared laser calibrated at the same intensity and using the relation:

$$G = \text{UFSD Signal Area} / \text{PiN Signal Area} . \quad (4)$$

Results of the gain measurement are shown in Fig. 7. The exponential dependence of the gain from the electric field (as mentioned in Section 3.1), and thus on the applied reverse bias, is visible. Two important observations emerge from the measurement:

- i Boron implant obtained with low temperature of diffusion has a higher gain with respect to the Boron with a high diffusion temperature, due to the different profile of the gain implant, as predicted by the simulation [18] and shown in Fig. 5;
- ii The presence of Carbon in the gain implant volume reduces the gain, and the reduction is more evident in sensors with a Gallium implant, as already anticipated in Section 3.3.

4.2. Time resolution measurement

The time resolution of a detector, σ_t , results from the sum of different contributions [1], (i) the Jitter, σ_{Jitter} , (ii) the Landau Time Walk, σ_{TimeWalk} , the Landau noise, $\sigma_{\text{Landau Noise}}$ and (iv) the signal distortion $\sigma_{\text{Distortion}}$:

$$\sigma_t^2 = \sigma_{\text{Jitter}}^2 + \sigma_{\text{TimeWalk}}^2 + \sigma_{\text{Landau Noise}}^2 + \sigma_{\text{Distortion}}^2 . \quad (5)$$

For the results presented here, the following simplification can be applied:

- i The effect of time walk can be compensated using a Constant Fraction Discriminator (CFD) analysis;
- ii In silicon sensors the shape of the signal can be calculated using Ramo's theorem, which states that the signal induced by a charge carrier is proportional to the drift velocity of the charge, v_{drift} , and to the weighting field inside the sensor volume, E_w , according to $i \propto q \cdot v_{\text{drift}} \cdot E_w$, where q is the charge generated in the detector volume. The signal distortion can be neglected if the drift velocities of electrons and holes are saturated and if the weighting field is very uniform.

Assuming these simplifications, the predominant terms of the time resolutions are Jitter and Landau noise.

4.2.1. Jitter

The jitter term represents the time uncertainty caused by the presence of noise, and it is influenced by the signal steepness. In particular, it is directly proportional to the noise, N , and inversely proportional to the slope of the signal, dV/dt : $\sigma_{\text{Jitter}} = N / (dV/dt)$.

Fig. 8 shows the measurement of the jitter as a function of the bias voltage, obtained using the TCT setup and calibrating the laser intensity to replicate the most probable value of the charge generated by a MIP passing through the sensor. The jitter decreases increasing the reverse bias, and therefore the gain of the sensor, reaching the value of 20 ps at the highest voltages.¹ The measurement of the jitter has been performed on sensors with three different configurations of the gain layer: (i) Boron Low Diffusion, (ii) Boron, and (iii) Gallium + Low Carbon. These results show that different doping strategies of the gain layer do not affect the timing performance of the sensors, and setting the proper reverse bias according to the different gain layer implants brings to the same ultimate jitter of ~ 15 ps.

4.2.2. Landau noise

Ionising particles crossing a silicon sensor create different energy depositions on an event-by-event basis, determining amplitude and shape variations on the current signals, that follow the Landau statistics. Landau noise contribution decreases with sensor thickness and reaches ~ 30 ps for 50 μm thick detectors.

Time resolution from MIP crossing the UFSD has been measured in a beam test at CERN SPS [19], using a beam of 180 GeV/c pions. The trigger used for the data acquisition was a previously calibrated 50 μm thick UFSD [5], with a time resolution of 35 ps. The time resolution measured making use of a CFD analysis is shown in Fig. 9 as a function of the gain, for the same sensors presented in Section 4.2.1, with gain layer configurations based on (i) Boron Low Diffusion, (ii) Boron, and (iii) Gallium + Low Carbon. Superimposed to the results, also the measured jitter terms (Fig. 8) plus the Landau noise contributions of ~ 30 ps, as predicted for 50 μm thick sensors [1], are shown.

Only the sensor with Gallium + Low Carbon configuration reaches the expected time resolution of ~ 40 ps at the highest gain (~ 40). The two sensors with Boron gain layer have a further contribution that worsen the overall time resolution by ~ 10 ps. The reason might be related to the hole effect that will be discussed in Section 4.3. More information on this contribution will come from beam tests with a tracking system.

From the obtained results it is possible to assert that the addition of Carbon in the gain layer volume does not affect the timing performance of the UFSD.

4.3. The hole effect

Most of the sensor structures designed for the FBK UFSD2 production, have an opening on the metallisation that contact the n^{++} ohmic implant, the so-called optical window (hole), to allow for laser tests on the sensors.

It has already been observed on devices manufactured by other foundries [20], that the amplitude and the time of arrival (ToA) of the signals from the two different regions, the one covered by the metal and the one with the opening, differ. More in detail, the signals generated on the volume below the opening region have a smaller amplitude (~ 10 mV less) and arrive tens of picoseconds later than the signal generated on the sensor volume covered by the metal.

To investigate if also the FBK UFSD2 production suffers the same amplitude and ToA differences between the metal–non metal regions, an edge-TCT scan has been performed on a $1 \times 1 \text{ mm}^2$ sensor, confirming the results observed on sensors from other producers. Results on time of arrival are reported on Fig. 10: the signals from the region with the opening on the metallisation arrive ~ 20 ps later than the signals

¹ The setup used for timing measurement introduces a contribution to the timing resolution, as the distance between the sensors mounted on the PCB and the amplifiers is more than 1 cm, and this long path of the signal to the amplification add a parasitic capacitance that worsen the timing performance of the system. It could be possible to gain ~ 5 ps using a more performing setup, with no parasitic capacitance.

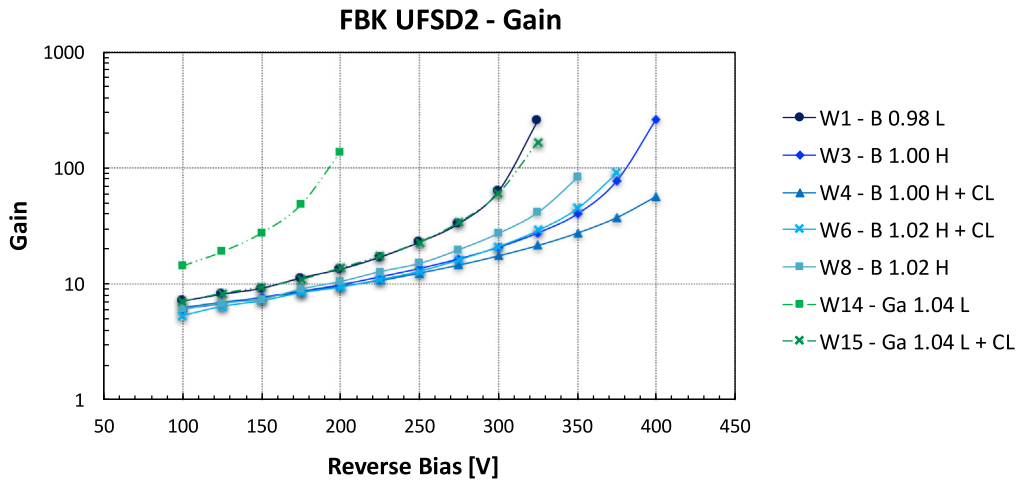


Fig. 7. Gain measurement as a function of the applied reverse bias for UFSD with different gain layer strategies: Boron Low Diffusion, Boron, Boron + Low Carbon, Gallium and Gallium + Low Carbon. Lines are added to interpolate between the measured points.

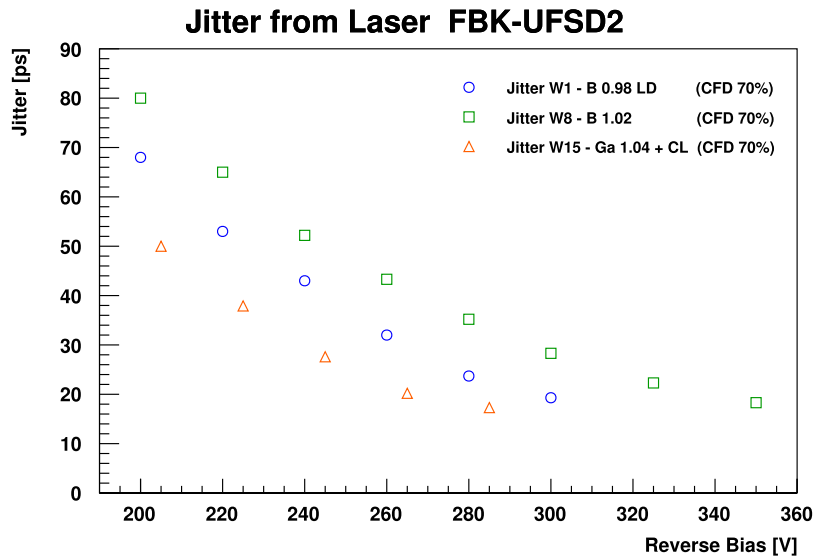


Fig. 8. Jitter as a function of the applied reverse bias measured on sensors with three different gain layer configurations: Boron Low Diffusion (blue dots), Boron (green squares) and Gallium + Low Carbon (orange triangles).

generated below the metallisation.² The effect still need to be understood and it is not reproduced by device simulation, but for the UFSD usage in experiments sensors fully covered by metallisation are foreseen.

A small ToA asymmetry with respect to the middle of the detector might be due to a little (~1 μm over the full sensor length) misalignment between the sensor plane and the laser system.

4.4. Intra-Pad inactive region

Pixelated and multi-strip UFSD structures need to have a good gain termination at the edge of each pixel. An important parameter for such structures is the size of the inactive region between two adjacent active pads. The measurement of the inactive region magnitude in FBK UFSD2 production has been performed on strip sensors using the edge-TCT technique. The strips under test have a width of 150 μm and a pitch of 200 μm .

² The time of arrival is defined as the difference between the time given from the trigger signal of the laser and the time of the signal generated inside the sensor, both taken at 50% of their amplitude.

Fig. 11 shows the results of the edge-TCT: from the 2D plot (top) is visible the separation between two adjacent strips, showing that the two strips are well isolated, while the 1D plot (bottom) of the signal amplitude readout from two different channels projected along the p^{++} sensor electrode (see top plot for reference) shows that the full width of the inactive region at half height of the maximum amplitude is about 60 μm , in agreement with the expectations from the sensor layout.

5. Radiation tolerance of FBK UFSD2 production

FBK UFSD2 sensors have been irradiated with neutrons at the JSI TRIGA reactor in Ljubljana [21] and with 24 GeV/c protons at the IR-RAD CERN irradiation facility [22] to test their radiation tolerance and identify the most radiation resistant strategy for the gain layer (for more details, see [23]). Sensors with different gain layer configurations have been irradiated: (i) Boron Low Diffusion, (ii) Boron, (iii) Boron + Low Carbon, (iv) Gallium, and (v) Gallium + Low Carbon. Investigated wafers and fluences are summarised in Table 3.

UFSD devices under irradiation suffer for removal of acceptor atoms in the gain layer volume [11]. The initial acceptor removal, N_A , is

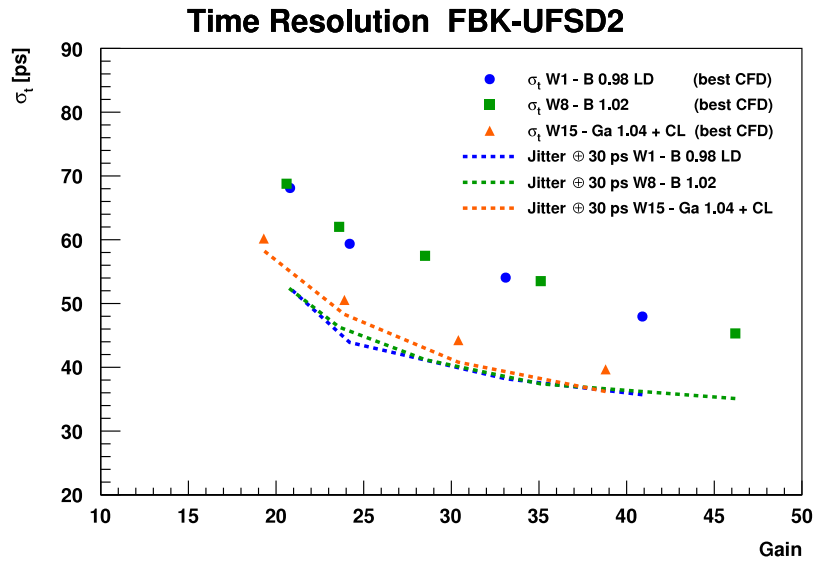


Fig. 9. Time resolution as a function of the gain measured on sensors with three different gain layer configuration: Boron Low Diffusion (blue dots), Boron (green squares), and Gallium + Low Carbon (orange triangles). Jitter contributions plus the predicted Landau noise of ~30 ps are also shown as dotted lines.

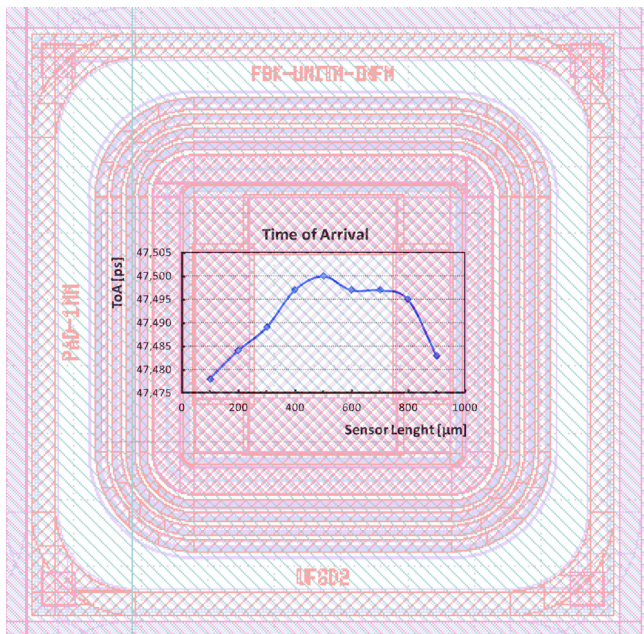


Fig. 10. Time of arrival of the signal along the length of the sensor, using the edge-TCT technique. The CAD image of the 1 × 1 mm² pad is superimposed. According to the figure, the laser is shot from the top margin of the sensor.

Table 3

Summary of wafers and fluences used in the irradiation campaign.

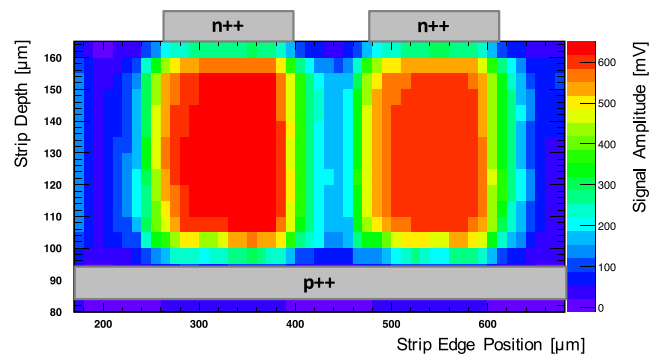
Wafer #	Dopant	Dose	n fluence [10 ¹⁵ n _{eq} /cm ²]	p fluence [10 ¹⁵ p/cm ²]
1	B LD	0.98	0.2, 0.4, 0.8, 1.5, 3.0, 6.0	0.6, 3.0
6	B + C Low	1.02	0.2, 0.4, 0.8, 1.5, 3.0, 6.0	0.6, 3.0
8	B	1.02	0.2, 0.4, 0.8, 1.5, 3.0, 6.0	0.6, 1.0, 3.0, 6.0
14	Ga	1.04	0.2, 0.4, 0.8, 1.5, 3.0, 6.0	0.6, 3.0
15	Ga + C Low	1.04	0.2, 0.4, 0.8, 1.5, 3.0, 6.0	0.6, 3.0

exponentially dependent on the irradiation fluence, ϕ , according to:

$$N_A(\phi) \propto N_A(0) \cdot e^{-c \frac{\phi}{\phi_0}}, \tag{6}$$

where $N_A(0)$ is the initial doping concentration, $\phi_0 = 1$ particle/cm² is a normalisation factor, and c is the acceptor removal coefficient.

2D scan of two adjacent strip edge



Signal amplitude projection - 2 strips

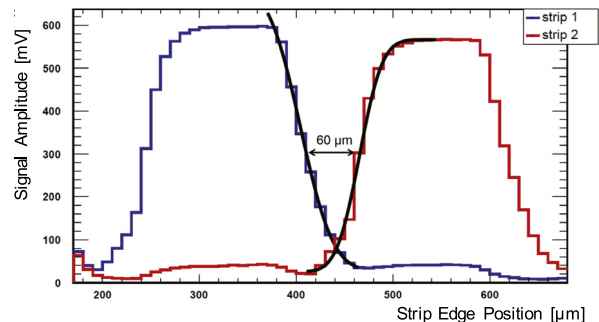


Fig. 11. Edge-TCT scan on a multi-strip sensor. Top: 2D scan of the sensor edge, strip position on the x axis and strip depth on the y axis. Bottom: 1D projection of the signal amplitude along the strip position, the amplitude as read from two different strips is shown.

To estimate the reduction of the gain layer dopant dose, the removal coefficient c from Eq. (6) has been studied. The survival active acceptor concentration has been estimated using the $C-V$ measurement: from the voltage at which the gain layer is depleted (drop of the capacitance, as shown in Fig. 4), it is possible to estimate the fraction of gain layer dopant still active in the sensors, as a function of the received fluence. By an exponential fit to the data points, the c coefficient has been extracted.

Fig. 12 shows the values of the initial acceptor removal coefficient, c , for sensors irradiated with neutrons. Together with the 5 different

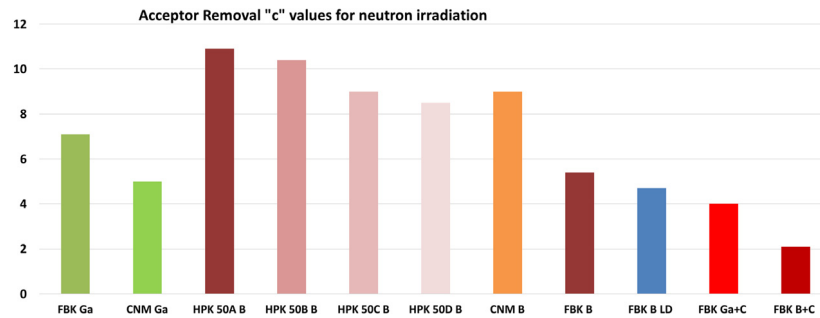


Fig. 12. Initial acceptor removal coefficient c from neutron irradiation of UFSD manufactured by 3 different foundries (CNM, FBK, and HPK) with different gain layer doping compositions. Effective c values are multiplied by 10^{-16} .

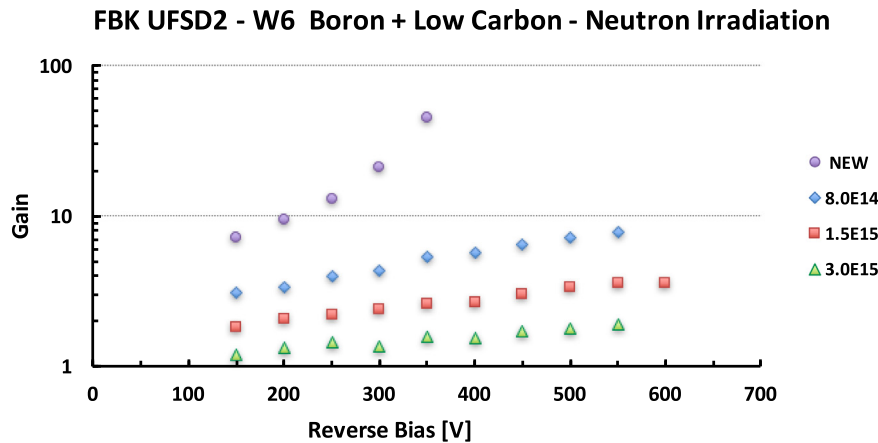


Fig. 13. Gain due to the gain layer implant for sensors with Boron + Low Carbon after reactor neutron irradiation, for a not-irradiated sensor and for three irradiation fluences: $8.0E14$, $1.5E15$, and $3.0E15$ n_{eq}/cm^2 .

configurations of the gain layer for the sensors produced by FBK, namely Boron Low Diffusion, Boron, Boron + Low Carbon, Gallium, and Gallium + Low Carbon, the plot reports also measurements for sensors manufactured by CNM, with Boron and Gallium, and by HPK, with 4 different doses of Boron implant [24]. The value of c is higher for sensors with Gallium, meaning that those sensors are more prone to suffer from initial acceptor removal under irradiation, while the lowest values of c are reached for sensors with Low Carbon concentration underneath the gain implant. In particular, the configuration Boron + Low Carbon is the most radiation tolerant, more than a factor of two with respect to the case without Carbon. Among sensors without Carbon, the gain implant with Boron diffused at low temperature is the most radiation hard.

Besides the study of the acceptor removal coefficient, the gain due to the survived gain layer implant has been studied, for all the configurations tested under irradiation. During the irradiation campaign both PiN diodes and UFSD have been irradiated at the same fluences, and the signal multiplication due to the gain layer contribution only is defined as:

$$G = \text{Irradiated UFSD Signal Area} / \text{Irradiated PiN Signal Area} . \quad (7)$$

Fig. 13 shows the gain due to the gain implant contribution only for sensors with Boron plus Low Carbon concentration in the implant volume, for the sensor before irradiation and after reactor neutron irradiation at three different fluences, namely $8.0E14$, $1.5E15$, and $3.0E15$ 1 MeV equivalent neutrons per cm^2 (n_{eq}/cm^2). It is possible to notice that even after $3.0E15$ n_{eq}/cm^2 , there is still a contribution to the signal multiplication due to the gain layer implant. Also, the irradiation extends the range of reverse bias at which it is possible to operate the detector, making accessible a region where the electric field in the bulk region is high enough to obtain charge multiplication along the whole sensor volume (see e.g. [8] for more details).

It has been observed that 24 GeV/c protons produce the same amount of initial acceptor removal as a function of the fluence (particles/ cm^2) as neutrons [23].

6. Conclusion

The first 50 μm thick UFSD production at FBK has been delivered. Several options for the gain layer implant have been pursued, in particular (i) Boron Low Diffusion, (ii) Boron, (iii) Boron plus Carbon, (iv) Gallium, and Gallium plus Carbon have been used to obtain a moderate internal charge multiplication.

Extensive tests and electrical characterisations on wafer show that the production is of very high quality. Further tests in laboratory prove that the internal gain, the timing performance and the extension of the inactive region between pads are as expected. Irradiation tests with neutrons and protons, assert that implanting Carbon atoms underneath the gain layer implant volume doubles the radiation resistance of the UFSD detectors, while for the configurations without Carbon, the Boron implant diffused at low temperature is the one responding better under irradiation.

Given the success of this first 50 μm thick UFSD production at FBK, the future plans are to further improve the radiation hardness of UFSD and to prove the capability to produce large area UFSD, to make FBK a good candidate for the sensor production for timing detectors foreseen for the ATLAS and CMS upgrade.

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