

2007 Activity Report The Last Year of Ph.D.

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Activity in “VLSI Design” which is my PhD Subject

- ◆ Contributed to the finalized design and simulation of CMAD ASIC in **AMS 350nm** technology for COMPASS experiment at CERN.
- ◆ Currently, I am working on a part (CP- PLL based serializer) of GigaBit Transceiver (GBT) for CERN- SLHC. What I do is to design the charge- pump phase locked loop based serializer in **IBM 130nm** technology.

Presentation, Publication, Conference @ 2007

- ◆ A Full Custom FE ASIC Prototype, CMAD COMPASS RICH- I Particle Detector System, 3rd Conference on Ph.d. Research in Microelectronics, July 2- 5, 2007, Bordoieux- France
- ◆ The ALICE- LHC On- line Data Quality Monitoring Framework : Present and Future, IEEE, 2007, Real Time Systems Conference, USA

Courses & Schools I attended

- ◆ CERN Accelerator School on Digital Signal Processing, 31- May - 9 June, 2007, Sigtuna/ Sweden (50 hours)

To keep track of my PhD Studies

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could be used.
