

The VME base address of the LTM module is set at 0x90000000.
 To read the rates, follows these steps:

- 1) write the word 0x0 at the VME address 0x000004
- 2) write the word 0x1 at the VME address 0x000008
- 3) The MRPC rates (in Hz) are stored in seven registers :

VME addresses	Contents
0x00005C	rate of singles from Bottom chamber = S1
0x000064	rate of singles from Central chamber = S2
0x00006C	rate of singles from Top chamber = S3
0x000074	rate of S1 * S2
0x00007C	rate of S1 * S3
0x000084	rate of S2 * S3
0x00008C	rate of S1 * S2 * S3

provided that the OR LVDS signals are connected as in Fig. 1.

These rates are valid after 30 s and are update every 30 s.
 These are 16 bit rates. They saturate at 65535 Hz.

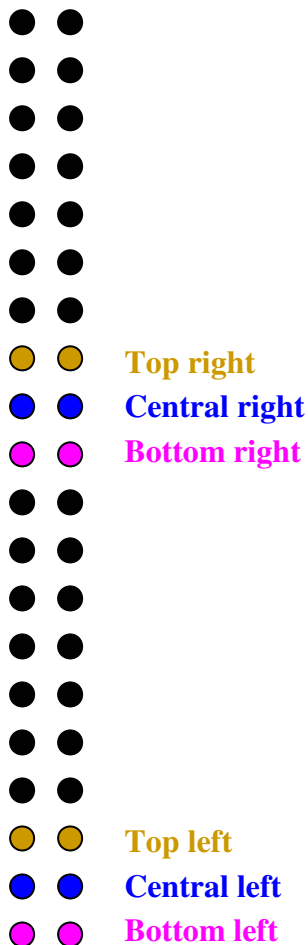


Fig. 1 OR connections